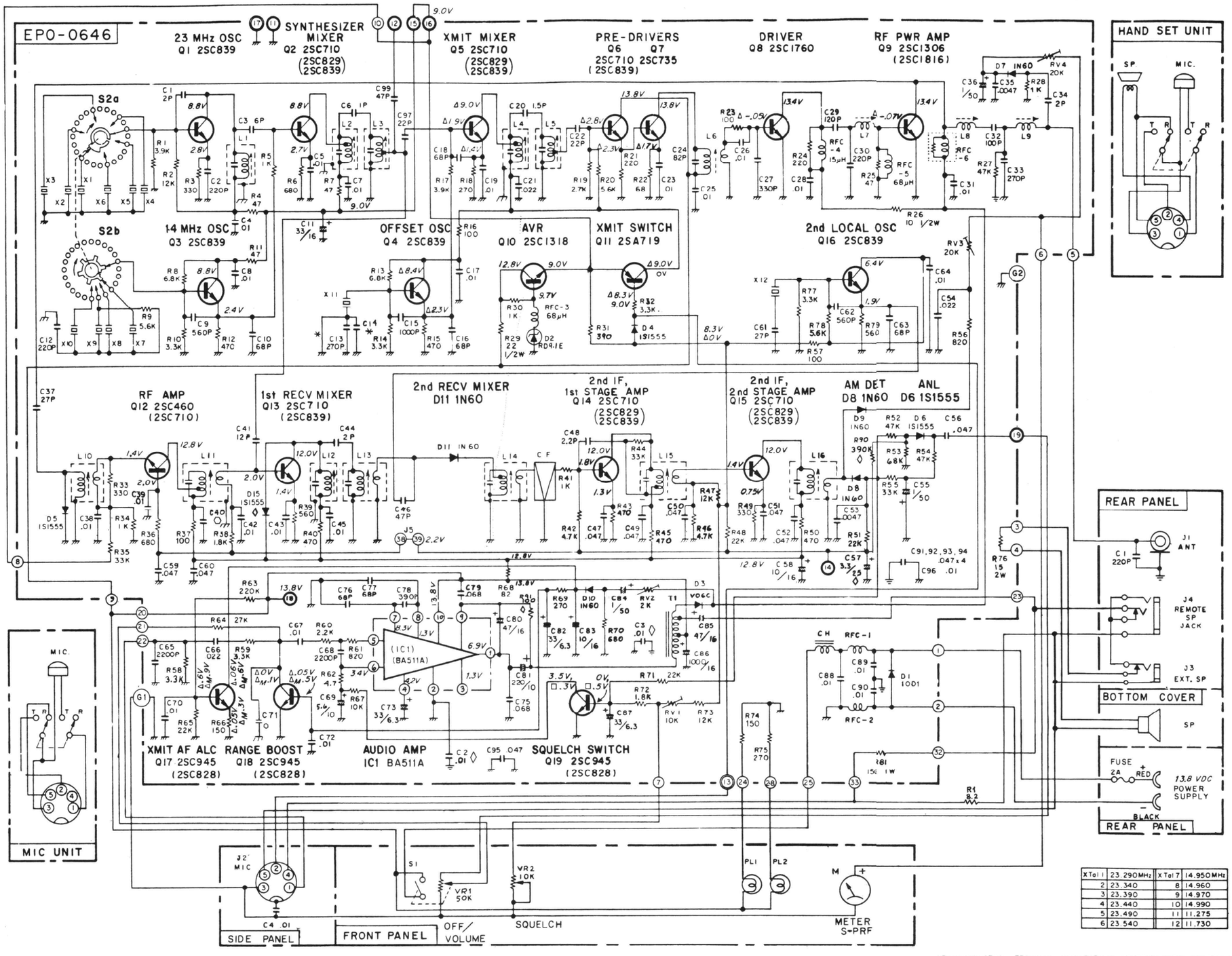

XTal	23.290MHz	XTal	14.950MHz
2	23.340	8	14.960
3	23.390	9	14.970
4	23.440	10	14.990
5	23.490	11	11.275
6	23.540	12	11.730

Figure 4-9. Schematic Diagram, 670B, Stage 2

1975 - 4 -16
4 -25
5 -26

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NOTES:

1. All voltages are measured with a VTVM (11 mega-ohms/V) from a p.c. board ground and with the transceiver connected to an external power supply adjusted for 13.8 VDC.

   All readings are measured with the transceiver in the receive mode with no signal and squelched (except as noted). All are in VDC (except as noted).

   If measurements obtained exceed ±20% of the indicated values, the cause of the difference should be corrected.

2. All resistors ¼ watt, 5%, except as noted.
   All capacitors in uF, except as noted.

3. Symbols used —

   □ — receive voltage, squelched
   Δ — transmit voltage, unmodulated
   Δ_M — transmit voltage, modulated (whistle)
   ⬡ — factory selected value
   ✳ — field-selectable component and/or value
   ⏚ — chassis ground
   ⏚ — p.c. board ground
   ◇ — component mounted on foil side of p.c. board
   ⬚ — unshielded, constructed component
   ⬚ — shielded component

SEMICONDUCTOR TERMINAL CONNECTION (BOTTOM VIEW UNLESS OTHERWISE NOTED)