



MRF428A

The RF Line

NPN SILICON RF POWER TRANSISTOR

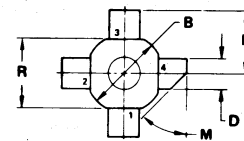
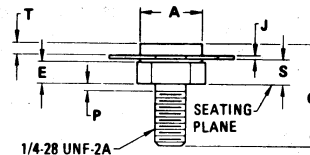
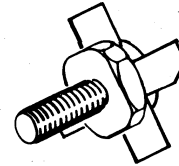
... designed primarily for high-voltage applications as a high-power linear amplifier from 2.0 to 30 MHz. Ideal for marine and base station equipment.

- Specified 50 Volt, 30 MHz Characteristics –
Output Power = 150 W(PEP)
Minimum Gain = 13 dB
Efficiency = 45%
- Intermodulation Distortion @ 150 W (PEP) –
IMD = -30 dB (Max)
- 100% Tested for Load Mismatch at all Phase Angles with
30:1 VSWR

150 W (PEP) – 30 MHz

RF POWER
TRANSISTOR

NPN SILICON



STYLE 1:
PIN 1. EMITTER
2. BASE
3. EMITTER
4. COLLECTOR

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	12.45	12.95	0.490	0.510
B	15.62	16.13	0.615	0.635
C	18.92	21.46	0.745	0.845
D	6.10	6.60	0.240	0.260
E	4.06	4.57	0.160	0.180
J	0.08	0.18	0.003	0.007
K	12.32	13.08	0.485	0.515
M	45° NOM		45° NOM	
P	—	1.27	—	0.050
R	13.72	14.22	0.540	0.560
S	4.83	5.33	0.190	0.210
T	2.03	2.54	0.080	0.100

CASE 307-01

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	55	Vdc
Collector-Base Voltage	V_{CBO}	110	Vdc
Emitter-Base Voltage	V_{EBO}	4.0	Vdc
Collector Current – Continuous	I_C	20	Adc
Withstand Current – 10 s	—	30	Adc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	320 1.83	Watts W/ $^\circ\text{C}$
Storage Temperature Range	T_{stg}	-65 to +200	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.5	$^\circ\text{C}/\text{W}$

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ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage (I _C = 200 mA _{dc} , I _B = 0)	BV _{CEO}	55	—	—	V _{dc}
Collector-Emitter Breakdown Voltage (I _C = 100 mA _{dc} , V _{BE} = 0)	BV _{CES}	110	—	—	V _{dc}
Collector-Base Breakdown Voltage (I _C = 100 mA _{dc} , I _E = 0)	BV _{CBO}	110	—	—	V _{dc}
Emitter-Base Breakdown Voltage (I _E = 10 mA _{dc} , I _C = 0)	BV _{EBO}	4.0	—	—	V _{dc}
ON CHARACTERISTICS					
DC Current Gain (I _C = 5.0 mA _{dc} , V _{CE} = 5.0 V _{dc})	h _{FE}	10	30	—	—
DYNAMIC CHARACTERISTICS					
Output Capacitance (V _{CB} = 50 V _{dc} , I _E = 0, f = 1.0 MHz)	C _{ob}	—	200	250	pF
FUNCTIONAL TESTS					
Common-Emitter Amplifier Gain (V _{CC} = 50 V _{dc} , P _{out} = 150 W (PEP), I _{C(max)} = 3.32 A _{dc} , f = 30 MHz)	G _{pE}	13	15	—	dB
Output Power (V _{CE} = 50 V _{dc} , f = 30 MHz)	P _{out}	150	—	—	W PEP
Collector Efficiency (V _{CC} = 50 V _{dc} , P _{out} = 150 W (PEP), I _{C(max)} = 3.32 A _{dc} , f = 30 MHz)	η	45	—	—	%
Intermodulation Distortion (1) (V _{CE} = 50 V _{dc} , P _{out} = 150 W (PEP), I _C = 3.32 A _{dc})	IMD	—	-33	-30	dB
Electrical Ruggedness (V _{CC} = 50 V _{dc} , P _{out} = 150 W (PEP), I _{C(max)} = 3.32 A _{dc} , f = 30 MHz) VSWR 30:1 at all Phase Angles	No Degradation in Output Power				

(1) To Mil Std 1311 Version A, Test Method 2204B, Two Tone, Reference Each Tone.

FIGURE 1 - 30 MHz TEST CIRCUIT SCHEMATIC

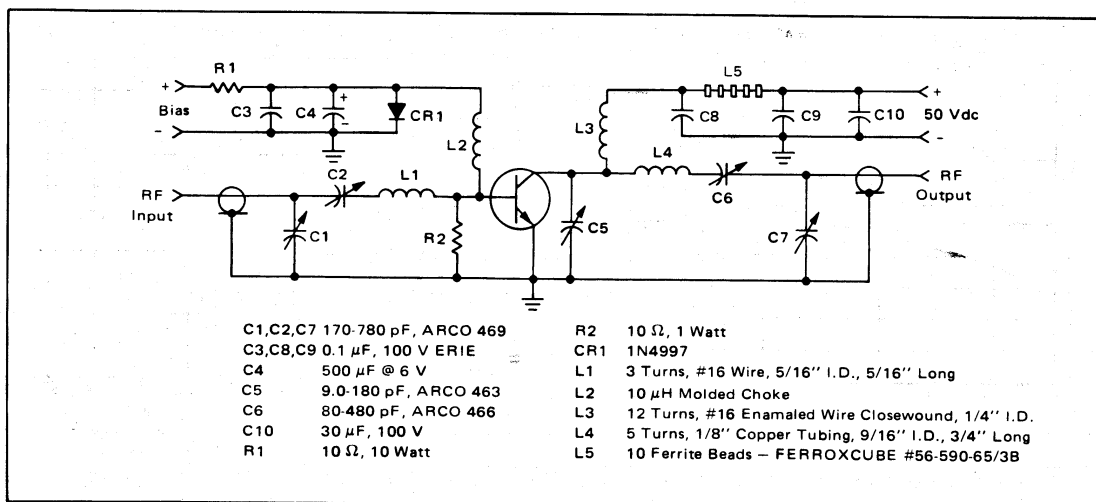




FIGURE 2 – OUTPUT POWER versus INPUT POWER

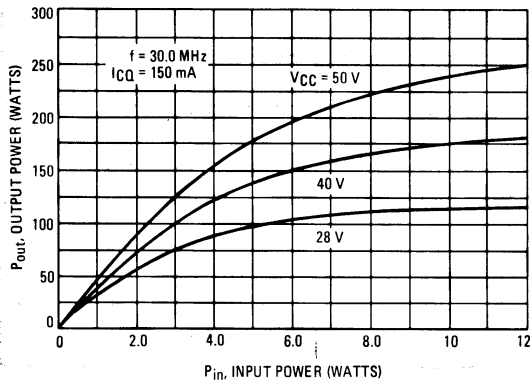


FIGURE 3 – OUTPUT POWER versus SUPPLY VOLTAGE

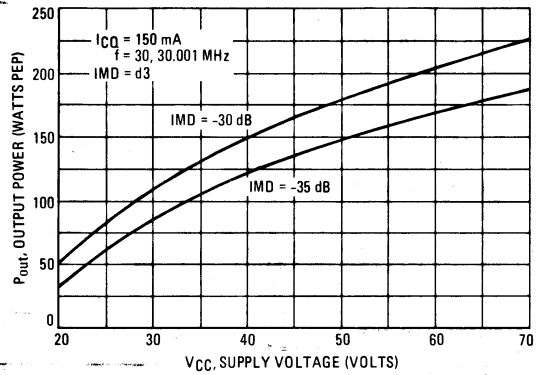


FIGURE 4 – POWER GAIN versus FREQUENCY

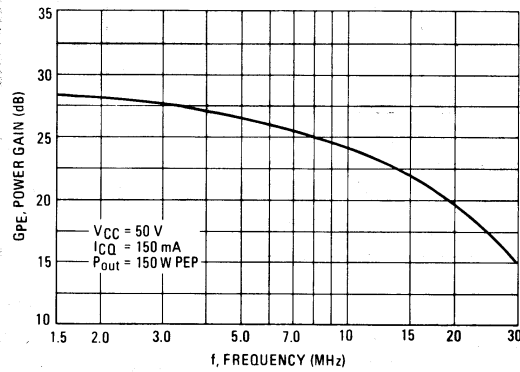
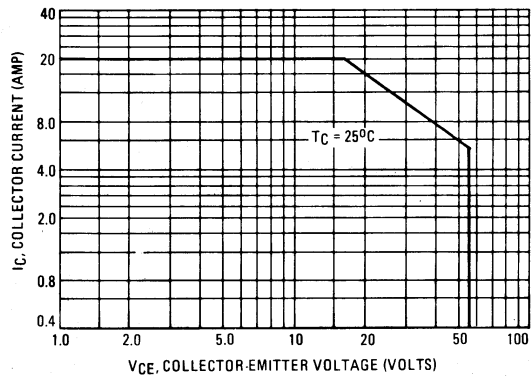


FIGURE 5 – DC SAFE OPERATING AREA



INTERMODULATION DISTORTION versus OUTPUT POWER

FIGURE 6 – $V_{CC} = 40 \text{ Vdc}$

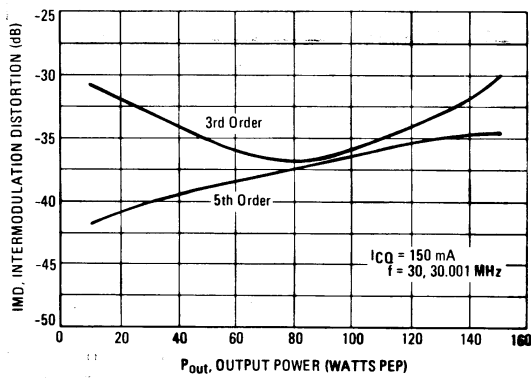
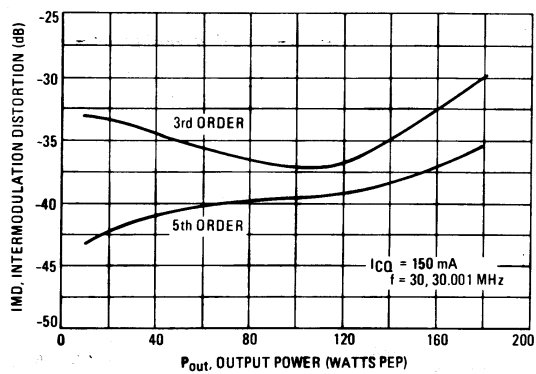


FIGURE 7 – $V_{CC} = 50 \text{ Vdc}$



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FIGURE 8 – OUTPUT CAPACITANCE versus FREQUENCY

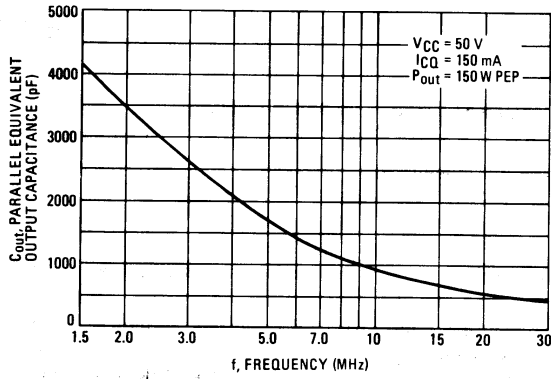
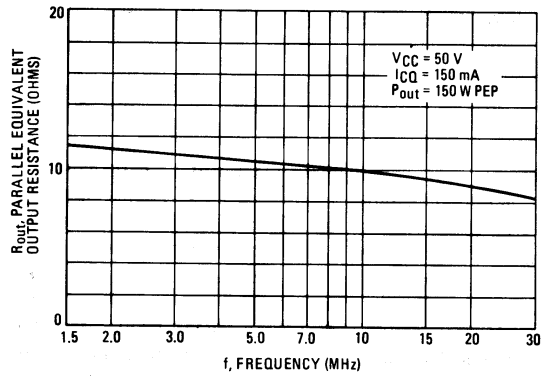


FIGURE 9 – OUTPUT RESISTANCE versus FREQUENCY



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FIGURE 10 – SERIES EQUIVALENT IMPEDANCE

