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Sharp CB-4470 Service Manual

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SERVICE MANUAL



'WARNING'

It is unlawful for the user to make any replacement or substitution of parts, adjustments or to service the transmitter by any one other than a person holding a commercial 1st or 2nd class radio operator's license. Any change in the circuitry that would change or violate the technical regulations or type acceptance is prohibited.

P.L.L. SYNTHESIZED 40 CHANNEL CITIZENS BAND TRANSCEIVER

MODEL CB-4470

This MODEL CB-4470 is fundamentally the same in circuitry design as the MODEL CB-4670 although it excludes the meter circuit and omits a part of the remote microphone unit from those of the CB-4670. In order to study what the "EQUIVALENT CIRCUIT OF IC", "HOW TO SET THE TRANSISTORS AND IC" and TROUBLE SHOOTING GUIDE are, refer to the counterparts of the MODEL CB-4670 SERVICE MANUAL.

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SHARP ELECTRONICS CORPORATION

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Parts Centers:				
P.O. Box 664	Paramus,	New Jersey	07652	(201) 265-5600
P.O. Box 20394	Long Beach.	Calif.	90801	(213) 830-4470

SPECIFICATIONS

Transmitter section	Speaker
RF power output 4W (maximum)	P.D.S. 8-ohm Imp.
Frequency range 27MHz Citizens Band	Semiconductor 5-ICs
Channels 40 chs. P.L.L. (Phase Locked	23-Transistors
Loop) circuit Synthesizer	29-Diodes
Type of crystal HC-18U	1-LED
Tolerance	2-Crystal
10.240MHz ± 0.003%	Dimensions Main unit
Transmitter modulation100% (maximum)	Width: 6-11/16"
Modulation limiter Yields high average modulation	Height: 2-1/2"
at average voice levels	Depth: 7-1/4"
Antenna matching 50 ohms Un-balanced	Speaker unit
Carrier deviation Not greater than ± 800Hz	Width: 3-15/16"
nominal on (exceeds F.C.C.,	Height: 3-15/16"
requirements)	Depth: 3-1/4"
Harmonic suppression Exceeds 60dB	Microphone unit
	Width: 2-5/8"
Receiver section	Height: 4-1/2"
Audio power output 3.5 Watts maximum power out-	Depth: 7/8"
put	Weight Main unit : 3.9 lbs.
Sensitivity $0.5\mu V/m$ for $10dB S + N/N$	Speaker unit: 1.5 lbs
ratio at 30% at 1000Hz modu-	Microphone unit: 0.3 lbs.
lation	Pocket size Remote Control Microphone Unit
Channels 40 chs. P.L.L. (Phase Locked	Microphone: Dynamic microphone (500 ohm)
Loop) circuit Synthesizer	Off-Volume control
Type of crystal HC-18U	Squelch control
$11.150MHz \dots \pm 0.003\%$	Press-to-talk switch
Selectivity 6dB down at ± 3kHz;	Channel selection switch: UP and DOWN type
$60 dB down at \pm 10 kHz$.	LED channel indicator blinking at emergency
Intermediate frequency1st-IF: 10.695MHz,	channel 9.
2nd-IF: 455kHz	Large scale LED channel indicator : 1-ch. ~ 40-ch.
Circuit type Dual conversion superhetero-	
dyne: Phase Locked Loop	Specifications are subject to change without prior notice,
(P.L.L.) frequency synthesizer	within FCC rules and regulations.
provides 40 transmit and receive	
channels.	
Auxiliary circuits Automatic noise limiter (ANL),	

Variable squelch

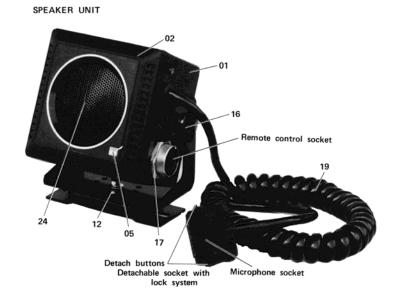
or base operation

Power source..... DC 12.0V Nominal

negative or positive ground
Antenna 50 ohm external antenna for car

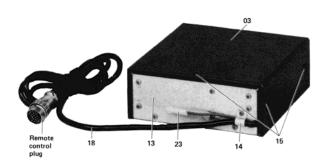
General

PARTS LAYOUT

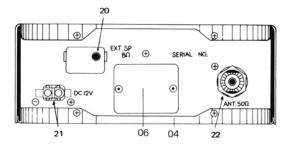




MAIN UNIT (FRONT)



MAIN UNIT (REAR)



- ①1) Cabinet, Speaker Side (GCABA1465AFSA)
- (02) Cabinet, Punching Metal Side (GCABB1465AFSA)
- (03) Cabinet, Main Unit, Black (GCABA3464AFSA)
- 04) Cabinet, Main Unit, Silver (GCABB3464AFFW)
- 05) Emblem "SHARP" (HINDM1080AFSB)
- 06 Indication Plate, Spec. (HINDM1182AFZZ)
- 07 Cabinet, Front, Microphone Unit (HPNLH0013AAZZ)
- 08) Cabinet, Rear, Microphone Unit (HPNLH0010AAZZ)
- (9) Up and Down (Channel Selection Switch) Knob (JKNBZ0001AAZZ)
- (1) Off-On Switch/Volume Control Knob (JKNBZ0002AAZZ) Squelch Control Knob (JKNBZ0002AAZZ)
- (11) Press-to-Talk Switch Knob (JKNBZ0003AAZZ)
- (12) Mounting Bracket Assembly, Speaker Unit (LBRC-0053AFSA)

- (13) Chassis, Front, Main Unit (LCHSS0126AFFW)
- (14) Holder, Remote Control Cable and Memory Cord (LHLDW3057AFFW)
- (15) Screw, Cabinet, Main Unit (LX-BZ0237AFFB)
- 16 Moulding Screw, $5\phi \times 15$ mm (LX-BZ0248AFZZ)
- 17 Nut, Remote Control Socket (24φ) (LX-NZ0123AFFN)
- (8) Remote Control Cable with Plug and Sockets Assembly (QCNW-0258AFZZ)
- Microphone Cable with Sockets Assembly (QCNW-0255AFZZ)
- 20 Jack, External Speaker, J101 (QJAKA0052AFZZ)
- 21) Plug, Power Supply, PG601 (QSOCZ2454AFZZ)
- 22) Socket, Antenna (50 ohms), SO401 (QSOCZ2470AFZZ)
- 23 Memory Cord with Socket, SO701 (QCNW-0254AFZZ)
- 24 Punching Metal, Speaker Unit (HPNC-0109AFSA)

Figure 1 PARTS LAYOUT

GENERAL DESCRIPTION (Refer to Figure 3)

RECEIVER SECTION

An input signal sent from the antenna is applied to the 1st-mixer (transistor Q2) via the RF amplifier (transistor Q1). Meanwhile, an oscillator signal delivered from the P.L.L. synthesizer is applied to the base of the transistor Q2 (1st-mixer) via the buffer circuit (transistor Q202). In this stage the above-mentioned input signal is converted to 1st-IF signal of 10.695 MHz.

This 1st-IF (10.695MHz) signal is supplied to the base of the transistor Q3 (2nd-mixer) through the transformers T3 and T4. Also to this transistor Q3 is supplied an oscillator signal (11.150MHz) from the transistor Q6, in which the signal is converted to 2nd-IF signal of 455kHz. The 2nd-IF (455kHz) signal is amplified by the 2nd-IF amplifier (transistors Q4 and Q5) and detected by the diode D2.

The output signal thus detected is applied to the terminal 6 of the integrated circuit IC-101 through the volume control (R925), amplified by the driver circuit and audio power amplifier circuit inside the IC-101 and finally applied to the speaker via the transformer T101.

TRANSMITTER SECTION

An audio signal sent from the microphone is applied to the terminal 6 of the integrated circuit IC101 so that it be audio-amplified and then applied to the final-stage transistor Q304 and drive-stage transistor Q303 via the transformer T101. Meanwhile, a carrier signal synthesized by the P.L.L. synthesizer circuit is amplified-by the 27MHz amplifier (transistors Q301 and Q302) and applied to the final-stage transistor Q304 through the drive-stage transistor Q303, in which it is modulated together with the aforesaid audio signal and finally transmitted through the antenna.

DESCRIPTION OF PHASE-LOCKED-LOOP (P.L.L.) CIRCUIT (Refer to Figure 2)

1) What is P.L.L.?

P.L.L. is abbreviation of Phase-Locked-Loop which synchronizes with frequency and phase of the stable standard input (crystal oscillation) given from the outside, namely working not only as automatic frequency control but also as automatic phase control.

The P.L.L. is now used to realize a synthesizer. Consisting of one crystal, the synthesizer serves as an oscillator to oscillate step by step (5kHz) in the receiver section range of 16.270MHz to 16.710MHz and the transmitter section range of 16.725 MHz to 17.165MHz.

Therefore, this synthesizer can be said to be on the same level in the connection with the accuracy and stability of oscillation as the crystal oscillator.

2) Frequency Synthesizer

The frequencies for both transmitter and receiver are synthesized by one crystal controlled oscillator and the Phase-Locked-Loop (or P.L.L.) consisting of eight basic building blocks: the divider (1/2) IC 201, the divider (1/1024) IC 201, the phase detector (phase comparator) IC 201, the low-pass filter IC 201, the voltage controlled oscillator (or V.C.O.) IC 202, the down mixer IC 202, the programmable divider IC 201 and the tripler T 201 as shown in Figure 2.

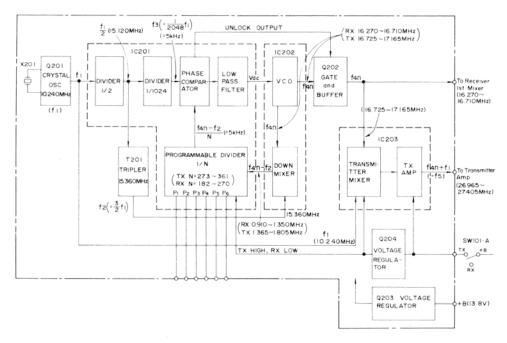


Figure 2 P.L.L. CIRCUIT FREQUENCY SYNTHESIZER

3) Frequency Determining (Refer to Figure 2 and Table 1)

- -1- The crystal oscillator consisting of a crystal X201 (10.240MHz) and transistor Q201 generates a basic frequency f1 (=10.240MHz).
- The basic frequency f_1 is applied to the fixed divided (1/2) network in the IC201 to be divided down to 5.120MHz signal (equivalent to 1/2 of the basic frequency f_1). The 5.120MHz signal is further divided down to a 5kHz ($f_3 = 1/2048 \cdot f_1$) signal by the fixed divided (1/1024) network and this frequency signal f_3 (5kHz) is applied to the input of phase comparator.
 - In addition to the above, the frequency signal f_1 (10.240MHz) is also applied to the transmitter mixer inside the IC 203 and the frequency signal $f_1/2$ (5.120MHz) is converted to a signal f_2 (15.360MHz) ($f_2 = 3/2 \cdot f_1$) by the tripler network (transformer T201) and this frequency signal f_2 is applied to the down mixer inside the IC 202.
- -3- Frequency signal f4n is the one that is generated by the voltage controlled oscillator (V.C.O.) inside the IC202 and this signal level is determined by DC voltage (Vdc) coming from the IC201. This frequency signal f4n is applied to the down mixer.
 - The following will describe how the signal fan generated by the V.C.O. serves to maek the P.L.L. (phase locked loop) circuit be locked.
- -4— The V.C.O. frequency signal f4n is mixed down with the above-mentioned signal f2 (15.360MHz) by the down mixer inside the IC 202, as a result of which there appears a mixed-down signal f4n-f2. This frequency signal f4n-f2 is applied to the programmable divider inside the IC 201.
- -5- The programmable divider (a portion of IC201) divides the frequency $f_4n f_2$ by the frequency divider number N (Receiver 182 to 270, transmitter 273 to 361), which is programmable by the channel selector bit of IC901 connected to the terminal pins 11 to 6 of IC201. The assigned number is shown in Table 1. The output frequency $(f_4n f_2)/N$ (near 5kHz) of the programmable divider is applied to another input of the phase comparator.
- -6- The phase comparator (IC201) compares the frequency f3 (=5kHz) and the other frequency (f4n-f2)/N from the programmable divider and generates a DC voltage Vdc (voltage control signal) proportional to the phase differences of both frequencies. The signal Vdc moves downward when (f4n-f2)/N goes higher then f3 and moves upward when (f4n-f2)/N goes lower than f3. When (f4n-f2)/N equals to f3, the Vdc does not move.

 The voltage signal Vdc from the output of phase comparator goes back to the V.C.O. (voltage controlled oscillator) IC 202 via the low-pass filter. Then the closed feedback loop is established.
- -7- In this method, a closed-loop frequency-feedback system, which is so called P.L.L., is formed and the frequency f4n of V.C.O. IC202 is locked.
- -8- When the P.L.L. is in lock, the two input signal frequencies to the phase comparator input are equal. Therefore the frequency fan is determined as follows:

The Receiver Frequency

```
f_4n = N \times f_3 + f_2

where f_2 = 15.360 \,\text{MHz} \ (=3/2 \, f_1)

f_3 = 5 \,\text{kHz} \ (=1/2048 \, f_1)

N = 182 \, \text{to} \ 270 \, \dots Determined channel select bit of IC901 as shown in Table 1.

For example, the frequency f_4n of "channel 1" is calculated as follows:

f_4n = 182 \times 0.005 + 15.360 \, (\text{MHz})

= 16.270 \, (\text{MHz})
```

Namely "N=182" is assigned for "channel 1". This frequency f4n is applied to the first mixer Q2 of receiver and the mixer IC203 of transmitter through the buffer amplifier Q202 and the filter coils T205 and T206.

The Transmitter Frequency

```
(1) f_{4}n = N \times f_{3} + f_{2}
where f_{2} = 15.360 \text{ MHz}
f_{3} = 5 \text{ kHz}
```

N = 273 to $361 \dots$ Determined by channel select bit of IC901 as shown Table 1.

(During the transmission, switching signal becomes high level (DC) so that the frequency divider number N is changed from one to another and then the number will be applied to the programmable divider.)

For example, the frequency f_{4n} of "channel 1" is calculated as follows:

```
f_{4n} = 273 \times 0.005 + 15.360 \text{ (MHz)}
= 16.725 (MHz)
```

Namely "N=273" is assigned for "channel 1".

This frequency f_{4n} is applied to the first mixer Q2 of receiver and the mixer IC 203 of transmitter through the buffer amplifier Q202 and the filter coils T205 and T206.

(2) The transmitter frequency f_5 is determined by mixing the frequency f_{4n} and the frequency f_1 (= 10.240 MHz).

```
f_5 = f_4 n + f_1
= N \times f_3 + f_2 + f_1
where f_1 = 10.240 \text{ MHz}
f_2 = 15.360 \text{ MHz}
f_3 = 5 \text{ kHz}
N = 273 \text{ to } 361
```

For example, the frequency fs of "channel 1" is calculated as follows: $fs = 273 \times 0.005 + 15.360 + 10.240 \text{ (MHz)}$ = 26.965 (MHz)

Table 1 shows the synthesized frequencies for each channel.

- -9- The gate and buffer circuit made of transistor Q202 works to prevent emission of unnecessary waves when the P.L.L. circuit is unlocked.
- -10- The frequency divider number N of programmable divider is decided by the value set by either of the channel selector LSI (IC904). In any of the channels, it is designed that the frequency divider number NT at the transmission is larger than that NR at the reception by a difference of 91.

 $N_R = N_T - 91$

FREQUENCY OF SYNTHESIS CHART —

							RE	CEIVER				TRANSMIT	ΓER
CHANNEL	fs (MHz)	fı (MHz)	f ₂ (=3/2f ₁) (MHz)	f ₃ (=f ₁ /2048) (kHz)	NR	fan (MHz)	f4 n-f 2 (kHz)	fs-f4 n (=f6) (MHz)	f7 (MHz)	f7-f6 (=f8) (kHz)	NT ·	f4 n (MHz)	f4 n-f 2 (kHz)
1	26.965	10.240	15.360	5	182	16.270	910	10.695	11.150	455	273	16.725	1365
2	26.975	10.240	15.360	5	184	16.280	920	10.695	11.150	455	275	16.735	1375
3	26.985	10.240	15.360	5	186	16.290	930	10.695	11.150	455	277	16.745	1385
4	27.005	10.240	15.360	5	190	16.310	950	10.695	11.150	455	281	16.765	1405
5	27.015	10.240	15.360	5	192	16,320	960	10.695	11.150	455	283	16.775	1415
6	27.025	10.240	15.360	5	194	16.330	970	10.695	11.150	455	285	16.785	1425
7	27.035	10.240	15.360	5	196	16.340	980	10.695	11.150	455	287	16.795	1435
8	27.055	10.240	15.360	5	200	16.360	1000	10.695	11.150	455	291	16.815	1455
9	27.065	10.240	15.360	5	202	16.370	1010	10.695	11.150	455	293	16.825	1465
10	27.075	10.240	15.360	5	204	16.380	1020	10.695	11.150	455	295	16.835	1475
11	27.085	10.240	15.360	5	206	16.390	1030	10.695	11.150	455	297	16.845	1485
12	27.105	10.240	15.360	5	210	16.410	1050	10.695	11.150	455	301	16.865	1505
13	27.115	10.240	15.360	5	212	16.420	1060	10.695	11.150	455	303	16.875	1515
14	27.125	10.240	15.360	-5	214	16.430	1070	10.695	11.150	455	305	16.885	1525
15	27.135	10.240	15.360	5	216	16.440	1080	10.695	11.150	455	307	16.895	1535
16	27.155	10.240	15.360	5	220	16.460	1100	10.695	11.150	455	311	16.915	1555
17	27.165	10.240	15.360	5	222	16.470	1110	10.695	11.150	455	313	16.925	1565
18	27.175	10.240	15.360	5	224	16.480	1120	10.695	11.150	455	315	16.935	1575
19	27.185	10.240	15.360	5	226	16.490	1130	10.695	11.150	455	317	16.945	1585
20	27.205	10.240	15.360	5	230	16.510	1150	10.695	11.150	455	321	16.965	1605
21	27.215	10.240	15.360	5	232	16.520	1160	10.695	11.150	455	323	16.975	1615
22	27.225	10.240	15.360	5	234	16.530	1170	10.695	11.150	455	325	16.985	1625
23	27.255	10.240	15.360	5	240	16.560	1200	10.695	11.150	455	331	17.015	1655
24	27.235	10.240	15.360	5	236	16.540	1180	10.695	11.150	455	327	16.995	1635
25	27.245	10.240	15.360	5	238	16.550	1190	10.695	11.150	455	329	17.005	1645
26	27.265	10.240	15.360	5	242	16.570	1210	10.695	11.150	455	333	17.025	1665
27	27.275	10.240	15.360	5	244	16.580	1220	10.695	11.150	455	335	17.035	1675
28	27.285	10.240	15.360	5	246	16.590	1230	10.695	11.150	455	337	17.045	1685
29	27,295	10.240	15.360	5	248	16.600	1240	10.695	11.150	455	339	17.055	169
30	27.305	10.240	15.360	5	250	16.610	1250	10.695	11.150	455	341	17.065	170
.31	27.315	10.240	15.360	5	252	16.620	1260	10.695	11.150	455	343	17.075	171
32	27.325	10.240	15.360	5	254	16.630	1270	10.695	11.150	455	345	17.085	172
33	27.335	10.240	15.360	5	256	16.640	1280	10.695	11.150	455	347	17.095	173
34	27.345	10.240	15.360	5	258	16.650	1290	10.695	11.150	455	349	17.105	174
35	27.355	10.240	15.360	5	260	16.660	1300	10.695	11.150	455	351	17.115	175
36	27.365	. 10.240	15.360	5	262	16.670	1310	10.695	11.150	455	353	17.125	176
37	27.375	10.240	15.360	5	264	16.680	1320	10.695	11.150	455	355	17.135	177
38	27.385	10.240	15,360	5	266	16.690	1330	10.695	11.150	455	357	17.145	178
39	27.395	10.240	15.360	5	268	16.700	1340	10.695	11.150	455	359	17.155	179
40	27.405	10.240	15.360	5	270	16.710	1350	10.695	11.150	455	361	17.165	180

CRYSTAL

X1 crystal

 $11.150 \,\mathrm{MHz} = f_7$

X201 crystal

 $10.240\,\text{MHz} = f_1$

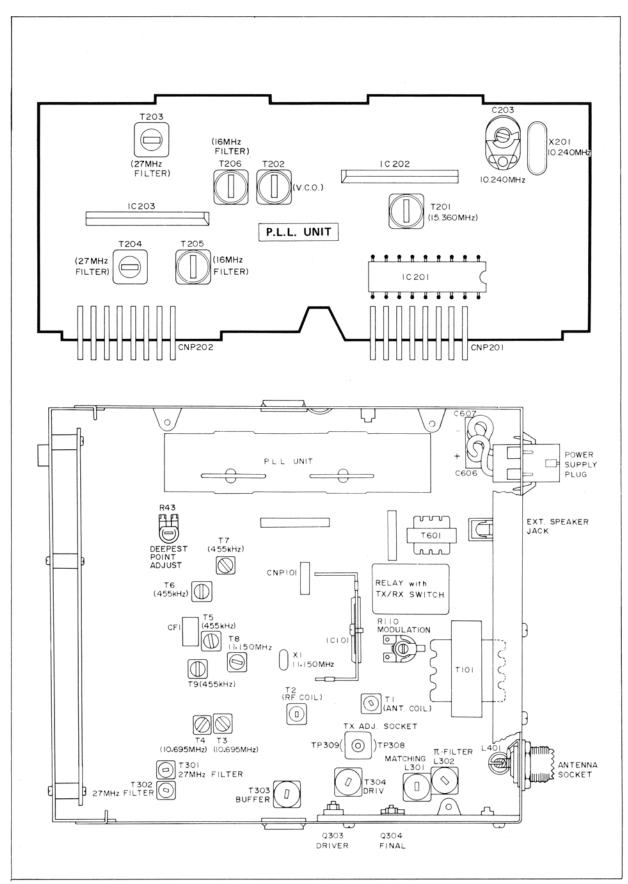


Figure 4 ALIGNMENT POINTS

ALIGNMENT

EQUIPMENT REQUIRED

Frequency Counter:

0 to 40MHz (High Sensitivity)

Synchroscope:

0 to 50MHz

Signal Generator:

10MHz to 30MHz with 1000Hz

AM mod.

Audio Signal Generator:

1000Hz (sine wave)

Audio Attenuator: RF Output Power Meter: 0 to 100dB 0 to 5W at 27MHz

RF Voltmeter:

0 to 3V, 0 to 50MHz

AC V.T.V.M.:

0 to 10V

DC V.T.V.M.: DC Milliammeter: 0 to 10V

0 to 500mA with Low-pass Filter

Dummy Load 8 ohms

and 50 ohms:

Spectrum Analyzer or

Field Strength Meter

CM Coupler

DC Power Supply:

13.8V, 2A

Non-inductive

[NOTE]

-1 - Keep supply voltage to 13.8V always during the alignment.

-2- The tools to be used for the alignment should be nonmetallic ones.

-3- Be sure to keep 50 ohms dummy load connectable with the antenna terminal all the way during the transmitter alignment.

-4- The main unit, speaker unit and microphone unit must be, when operated together, considered an independent set.

PHASE LOCKED LOOP (P.L.L.) CIRCUIT ALIGNMENT

	STEP	CONNECTION OF MEASURING INSTRUMENT	ADJUST- MENT	PROCEDURE
(1	1 10.240 MHz)	Connect a frequency counter, through 5PF capacitor, to the test point TP201 (Emitter of transistor Q201).	C203	Adjust so that the frequency counter reads within 10.240 MHz ± 300 Hz.
(1	2 5.360 MHz)	 Connect an RF voltmeter to the test point TP202 (the terminal No. 4 of IC 202). Connect a frequency counter, through 5PF capacitor, to the test point TP202. 	T201	Adjust so that the RF voltmeter reads the maximum. Make sure the frequency counter is reading within 15.360 MHz ± 450 Hz.
	(V.C.O.)	Connect a D.C. V.T.V.M. to the test point TP203.	T202	 Set the channel to "1". Adjust so that the D.C. V.T.V.M. reads exactly 2.0 V. Set the channel to "1" and/or "40" and make sure the D.C. V.T.V.M. reads within 2.0 V to 4.3 V.
(10	4 6MHz Filter)	Connect an RF voltmeter to the test point TP204. (the secondary of the transformer T205).	T205 T206	1) Set the channel to "40". 2) Adjust so that the RF voltmeter reads the maximum. (about 400 mV ± 200 mV)
(16	5 6 MHz Frequency)	Connect a frequency counter, through 5PF capacitor, to the test point TP204 (the secondary of the transformer T205).		1) Set the channel to "1". 2) Make sure the frequency counter is reading 16.270 MHz (RX) and 16.725 MHz (TX). 3) Set the channel to "40". 4) Make sure the frequency counter is reading 16.710 MHz (RX) and 17.165 MHz (TX).
TX	6 (27MHz Filter)	Connect a RF voltmeter to the test point TP205 (the secondary of the transformer T204).	T203 T204	1) Set the channel to "20". 2) Adjust so that the RF voltmeter reads the maximum. (about 2.5 V to 3.5 V)
	7 (27 MHz Frequency)	Connect a frequency counter, through 5PF capacitor, to the test point TP205 (the secondary of the transformer T204).		Set the channel to "20". Make sure the frequency counter is reading within 27.205 MHz ± 300 Hz.
	8 7MHz Frequency Readjust)	Same as step 7.	C203	 Set the channel to "20". Readjust so that the frequency counter reads within 27.205 MHz ± 300 Hz.

RECEIVER ALIGNMENT

STEP	CONNECTION OF MEASURING INSTRUMENT	ADJUST- MENT	PROCEDURE		
(11.150 MHz)	Connect a frequency counter, through 5PF capacitor, to the test point TPI. (Base of transistor Q3)	Т8	Adjust so that the frequency counter reads within 11.150 MHz ± 100 Hz. (The oscillation voltage then is about 90mV)		
(1st-IF and 2nd-IF)	1) Connect an AC V.T.V.M. to both sides of the speaker voice coil lug. 2) Connect a signal generator, through 0.01 MFD capacitor, to the test point TP2 (the secondary of the transformer T2). 3) Set the signal generator to 10.695 MHz, modulation 1000 Hz, 30%. NOTE: Be sure to connect the ground wire of signal generator to the ground of the antenna socket.	T3 T4 T5 T6 T7 T9	Adjust so that the AC V.T.V.M. reads the maximum. IF waveform can be said to be best adjusted when it becomes maximum in size and its band width is the widest with the central frequency similar to that of the ceramic filter. 1) Set the channel to "18".		
3 (RF)	 Connect the AC V.T.V.M. to both sides of the speaker voice coil lug. Connect the signal generator to the antenna socket. Set the signal generator to 27.175 MHz (18 channel), modulation 1000 Hz, 30%. 	T2 T1	Set the channel to "18". Adjust the AC V.T.V.M. until it reads the maximum.		
4 (Deepest Point of Squelch)	1) Connect a signal generator to the antenna socket, keeping the frequency of signal generator to 27.175 MHz ("18 channel") and modulation 1000 Hz, 30%. 2) Connect a low-frequency wattmeter to the external speaker jack.	R43 (30K ohms –B)	 Adjust the channel indication of the unit to "18" and set the volume control to "MAX" position. Set the output level of a signal generator to "60 dB" (1000 Hz, 30%). At the time make sure the low-frequency wattmeter reads approx. 4W (maximum). Next, adjust the volume control so that the low-frequency wattmeter indicates 500 mW. Rotate the squelch control knob of the unit fully clockwise. Adjust the semi-fixed resistor R43 so that the low-frequency output becomes 0.05W. 		

TRANSMITTER AND MODULATOR ALIGNMENT

- -1- When the set is made ready for the transmitting operation, be sure to always connect the RF output power meter and 50 ohms dummy load to the external antenna socket—this should never be forgotten even if it is not noted down specifically. If otherwise, the final transistor Q304 may be damaged.

 -2— When making the connection of measuring instruments, see Figure 5.

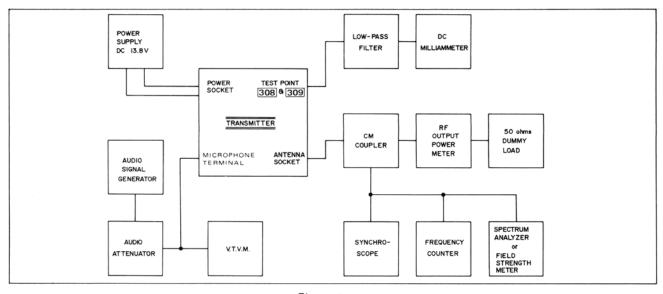


Figure 5

STEP	CONNECTION OF MEASURING INSTRUMENT	ADJUST- MENT	PROCEDURE
1 (27 MHz Filter)	Connect the synchroscope to the test point TP301 (Base of transistor Q302).	T301 T302	 Set the channel to "40". Adjust so that the maximum waveform (amplitude) appears on the synchroscope. Set the channel to "1" and/or "40" to make sure the waveform doesn't decrease in size. Loosen the core of T301 by 2 turns from a peak point where the output is maximum.
2 (Buffer)	 Remove the plug which have been inserted in the test points TP308 and TP309 of the set. Connect in turn DC milliammeter, through the RF rejection filter shown in Figure 6, to the test points TP308 and TP309. 	T303	1) Set the channel to "40". 2) Adjust so that the DC milliammeter connected to the test point TP309. reads the maximum. (Driver current)
3 (Driver)	Same as above.	T304	Adjust so that the DC milliammeter connected to the test point TP309 indicates the dip point. The amperage then is about 45 to 80 mA.
4 (Final)	Same as step 2, and connect the RF output power meter and 50 ohms dummy load to the antenna socket.	L301	Adjust so that the DC milliammeter connected to the test point TP308 reads 450mA ± 50mA (Final current).
5 (π-Filter)	Same as above.	L302	Adjust so that the RF output power meter reads the maximum. The reading then should not exceed 4W. (FCC Rules and Regulations Part 95, Section 95. 43.)
6	Repeat the steps 2 to 5 until the best results	will be obtain	ned.
7 (Modulation)	1) Connect the RF output power meter, 50 ohms dummy load and synchroscope, through CM coupler, to the antenna socket. 2) Connect a audio signal generator, attenuater and AC V.T.V.M. to the microphone terminal. 3) Keep the output of audio signal generator to 1000 Hz, 700 mV.	R110 (1K ohms -B)	 Set the channel to "20". Turn R110 counterclockwise until the modulation limiter circuit stops its function. Make sure there appears 700mV input signal at the microphone terminal from an audio signal generator. Depress the press-to-talk switch on the microphone. Adjust R110 so that the modulation factor of RF output waveform appeared on the synchroscope becomes 95 to 99% (See Figure 7). Set the attenuator to "-41dB" (6 mV). Make sure the modulation factor of RF output waveform on a synchroscope is more than 50%.

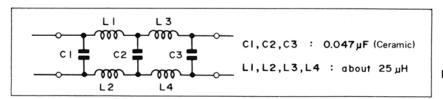


Figure 6 RF REJECTION FILTER (LOW-PASS FILTER)

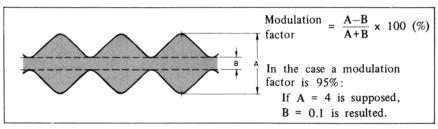


Figure 7

TRUTH TABLE FOR A RELATIONSHIP BETWEEN THE CHANNEL NUMBERS AND P.L.L. DATA OUTPUTS AND CHANNEL DISPLAY OUTPUTS

CHANNEL PI(8 s 1 0 2 1 3 0 4 0 5 1 6 0 7 1 8 1 9 0 10 1 11 0 12 0 13 1 14 0 15 1 16 1 17 0 18 1 19 0 20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0 29 1	0) P2(9) t 0 0 1 0 1 0 1 1 0 1 1 0 1 1 0 0 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 1 0 1 1 1 1 1 0 1	D P3((0)) u 0 0 1 1 1 0 0 1 1 1 1 0 0	v 0 0 0 0 0 1 1 1 1 0 0 0 0 0 0 1 1 1 1	Ps((2)) w 0 0 0 0 0 0 0 0 0 0 0 1 1 1 1 1 1	x 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	28) h 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 0 0 0 0 0 0	26 j 1 1 1 1 1 1 1 0 0 0 0 0 0 0	25) k 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	23) m 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	22) n 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	21) a 1 0 0 1 0 0 1 0 0 0 1 0 0 1 0 1 1 0 1	0 0 0 0 1 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	180 d 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 0 1 1 0 0 0 0 1 1 0	170 e 1 1 1 0 1 1 0 1 1 1 1 1 0 1 1 1 1 0 0 1 1 1 1 1 0 0 1 1 1 1 1 1 0 0 1 1 1 1 1 1 0 0 1 1 1 1 1 1 0 0 1	16 f 1 1 0 0 0 1 0 1 1 1 0 0 0 0 1 0 0 0 0 0	15) g 1 0 0 0 0 0 1 1 0 0 0 0 0 0 0 0 0 0 0 0	9-CH. OUT 7 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
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9 0 10 1 11 0 12 0 13 1 14 0 15 1 16 1 17 0 18 1 19 0 20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	1 1 0 1 1 0 0 1 0 0 1 0 0	0 0 1 1 1 0 0 0 1 1 1	1 1 1 1 0 0 0 0 0	0 0 0 0 0 1 1 1 1 1	0 0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1 1	1 0 0 0 0 0 0 0	1 0 0 0 0 0 0	1 1 1 1 1 1 1 1	1 1 1 1 1 1 1	1 1 1 1 1 1 1	1 1 1 1 1 1 1	0 0 1 0 0 1	0 0 0 0 0	0 0 0 1 0 0	1 0 1 0 0 1	1 0 1 0 1 1	0 0 1 1 1 0	0 1 1 0 0 0	1 0 0 0 0
10 1 11 0 12 0 13 1 14 0 15 1 16 1 17 0 18 1 19 0 20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	1 0 1 1 0 0 0 1 0 0	0 1 1 1 0 0 0 1 1 1	1 1 1 0 0 0 0 0	0 0 0 0 1 1 1 1 1	0 0 0 0 0 0 0 0	1 1 1 1 1 1 1 1	0 0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1	0 1 0 0 1	0 0 0 0 0	0 0 1 0 0	0 1 0 0 1	0 1 0 1 1 1 1	0 1 1 1 0 0	1 0 0 0	0 0 0 0 0
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13 1 14 0 15 1 16 1 17 0 18 1 19 0 20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	1 0 0 1 0 0 1 0	1 0 0 0 1 1 1	1 0 0 0 0 0 0	0 1 1 1 1 1	0 0 0 0 0 0	1 1 1 1 1	0 0 0 0 0	0 0 0 0	1 1 1 1	1 1 1	1 1 1	1 1 1 1	0 1 0	0 0 1	0 0	0 1 0	1 1 1	0 0	0 0 0	0 0
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15 1 16 1 17 0 18 1 19 0 20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	0 1 0 0 1 0	0 0 1 1 1 0	0 0 0 0 0	1 1 1 1	0 0 0 0 0	1 1 1	0 0 0	0 0	1 1 1	1	1	1	0	1	0	0	1	0	0	0
16 1 17 0 18 1 19 0 20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	1 0 0 1 0	0 1 1 1 0	0 0 0 0	1 1 1	0 0 0 0	1 1 1	0 0	0	1	1	1	1			-		-	-		
17 0 18 1 19 0 20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	0 0 1 0	1 1 1 0	0 0 0 1	1 1 1	0 0	1	0	0	1		-		1	1	0	0	0	0	0	0
18 1 19 0 20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	0 1 0 0	1 1 0	0 0 1	1	0	1	0			1	1	1								
19 0 20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	0 0	1 0	0	1	0		-	0	1				0	0	0	1	1	1	1	0
20 0 21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	0	0	1			1	0			1	1	1	0	0	0	0	0	0	0	0
21 1 22 0 23 1 24 1 25 0 26 0 27 1 28 0	0		-	1				0	1	1	1	1	0	0	0	1	1	0	0	0
22 0 23 1 24 1 25 0 26 0 27 1 28 0	+	0			0	0	0	1	0	0	1	0	0	0	0	0	0	0	1	0
23 1 24 1 25 0 26 0 27 1 28 0	1		1	1	0	0	0	1	0	0	1	0	1	0	0	1	1	1	1	0
24 1 25 0 26 0 27 1 28 0		0	1	1	0	0	0	1	0	0	1	0	0	0	1	0	0	1	0	0
25 0 26 0 27 1 28 0	0	1	1	1	0	0	0	1	0	0	1	0	0	0	0	0	1	1	0	0
26 0 27 1 28 0	1	0	1	1	0	0	0	1	0	0	1	0	1	0	0	1	1	0	0	0
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28 0	1 ,	1	1	1	0	0	0	1	0	0	1	0	1	1	0	0	0	0	0	0
	1	1	1	1	0	0	0	1	0	0	1	0	0	0	0	1	1	1	1	0
29 1	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0
	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0	1	1	0	0	0
30 0	1	0	0 .	0	1	0	0	0	0	1	1	0	0	0	0	0	0	0	1	0
31 1	1	0	0	0	1	0	0	0	0	1	1	0	1	0	0	1	1	1	1	0
32 0	0	1	0	0	1	0	0	0	0	1	1	0	0	0	1	0	0	1	0	0
33 1	0	1	0	0	1	0	0	0	0	1	1	0	0	0	0	0	1	1	0	0
34 0	1	1	0	0	1	0	0	0	0	1	1	0	1	0	0	1	1	0	0	0
35 1	1	1	0	0	1	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0
36 0	0	0	1	0	1	0	0	0	0	1	1	0	1	1	0	0	0	0	0	0
37 1	0	0	1	0	1	0	0	0	0	1	1	0	0	0	0	1	1	1	1	0
38 0	1	0	1	0	1	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0
39 1	1	0	1	0	1	0	0	0	0	1	1	0	0	0	0	1	1	0	0	0
40 0	0	1	1	0	1	1	0	0	1	1	0	0	0	0	0	0	0	0	1	0

Table 2

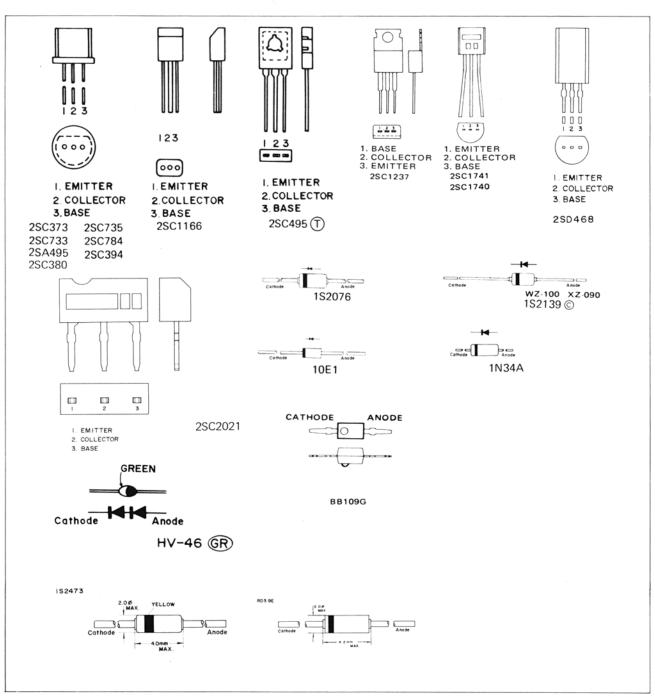


Figure 11 SEMICONDUCTORS BASING

REPLACEMENT PARTS LIST

"HOW TO ORDER REPLACEMENT PARTS"

To have your order filled promptly and correctly, please furnish the following informations.

3. PART NO.

1. MODEL NUMBER 2. REF. NO.

4. DESCRIPTION

Order to : Parts Center

P.O. Box 664 Paramus, New Jersey 07652 (201) 265-5600

P.O. Box 20394 Long Beach, Calif. 90801 (213) 830-4470

REF. NO.	PART NO.	DESCRIPTION	REF. NO.	PART NO.	DESCRIPTION
	INTEGRA	ATED CIRCUITS			
IC101	RH-IX1070AFZZ	Audio Power Amplifier	Q801	VS2SD468-C/-1	Channel Indication Driver (2SD468©)
10101	KII-IXIO70AI ZZ	(TA7205AP)	Q802	VS2SC2021//-1	9-channel Flashing (2SC2021)
IC201	RH-IX1067AFZZ	P.L.L. Synthesizer, Divider,	Q803	VS2SC2021//-1	9-channel Flashing (2SC2021)
10201	KII IXIOOTTII EE	Phase Comparator, Low-Pass	2000	102002021// 1	onamer i monthly (2502021)
		Filter and Programmable			
		Divider (TC9102P)			DIODES
IC202	RH-IX1068AFZZ	P.L.L. Synthesizer, V.C.O.			
		(Voltage Controlled Oscilla-	D1	VHD1S2076//-1	Static Protector (1S2076)
		tor) and Down Mixer	D2	VHD1N34A///-1	AM Detector (1N34A)
		(TA7310P)	D4	VHD1S2076//-1	A.N.L. (Automatic Noise
IC203	RH-IX1068AFZZ	Transmitter, 27MHz Mixer			Limiter) (1S2076)
10001	DILIVO0171177	and Amplifier (TA7310P)	D5	VHEWZ-100//1F	Zener Diode, Voltage Regulator
IC901	RH-IX0017AAZZ	C-MOS IC, Channel Selector	D.C	VIIID102076// 1	$(10V \pm 0.5V)$ (WZ-100)
		(M58476-141P)	D6	VHD1S2076//-1	Squelch (182076)
			D7 D8	VHD1S2076//-1	Squelch (1S2076)
	TRA	NSISTORS	D8	VHD1S2473//-1 VHVHV46-G//-1	A.V.C. (1S2473) Varistor, Squelch Stabilizer
				VIIVIIV40-G//-1	(HV-46 GR)
Q1	VS2SC784-R/1F	RF Amplifier (2SC784®)	D101	VHD1S2076//-1	Modulation Limiter (1S2076)
Q2	VS2SC394-Y/-1	1st-Mixer (10.695MHz) (2SC394 \widehat{Y})	D102	VHD1S2076//-1	Modulation Limiter (1S2076)
Q3	VS2SC380-O/-1	2nd-Mixer (455kHz)	D201	VHCBB109G//-1	Varicap, V.C.O. (BB109G)
Q3		(2SC380 ⁽⁾)	D202	VHC1S2139-C-1	Varicap, TX Shifter (1S2139©)
Q4	VS2SC380-Y/-1	IF (455kHz) Amplifier (2SC380 ♥)	D204	VHEXZ-090//-1	Zener Diode, Voltage Regulator, 9V±0.25V (XZ-090)
Q5	VS2SC380-Y/-1	IF (455kHz) Amplifier (2SC380 ①)	D205	VHEXZ-090//-1	Zener Diode, Voltage Regulator, 9V±0.25V (XZ-090)
Q6	VS2SC380-O/-1	Crystal (11.150MHz) Oscillator	D301	VHD1S2076//-1	Static Protector (1S2076)
		(2SC380 (Q))	D302	VHD1S2076//-1	Static Protector (1S2076)
Q7	VS2SC373-G/-1	AVC Amplifier (2SC373)	D601	VHD10E1////-1	Circuit Protector (10E1)
Q8	VS2SC733-BL-1	Squelch Voltage Amplifier	D602	VHD10E1////-1	Protector (10E1)
0.0		(2SC733 (BL))	D701	VHD1S2473//-1	Squelch Switching (1S2473)
Q9	VS2SC1740Q/-1	Squelch Switching (2SC1740Q)	D702	VHEXZ-090//-1	Zener Diode, Voltage Regulator, 9V ± 0.25V (XZ-090)
Q101	VS2SA495-Y/-1	Modulation Limiter	D703	VHD10E1///-1	Protector (10E1)
		(2SA495 ♥)	D704	VHD10E1////-1	Memory (10E1)
Q201	VS2SC373-G/-1	P.L.L. Synthesizer, Crystal	D705	VHD10E1///-1	Memory (10E1)
		(10.240MHz) Oscillator	D706	VHD10E1///-1	Memory (10E1)
Q202	VS2SC373-G/-1	(2SC373)	D708	VHD10E1///-1	Memory (10E1)
Q202	V 525C 5 / 5-G/-1	P.L.L. Synthesizer, Buffer and Gate (2SC373)	D709	VHD1S2473//-1	Memory (182473)
Q203	VS2SD468-C/-1	P.L.L. Synthesizer, Voltage	D710 D711	VHD1S2473//-1	Memory (182473)
Q203	15255400-07-1	Regulator (2SD468©)	D901	VHD1N34A///-1 VHERD3.9E-B//	Memory (1N34A)
Q204	VS2SC1741//-1	P.L.L. Synthesizer, Voltage			Zener Diode, Voltage Regulator, 3.7V ~ 4.1V (RD3.9E (B))
Q301	VS2SC735-Y/-1	Regulator, TX (2SC1741) Transmitter, Buffer Amplifier	LED901	RH-PZ0008AAZZ	LED (Light Emitting Diode), Channel Indicator (GL-7P201)
Q302	VS2SC1166-Y-1	(2SC735 ♥) Transmitter, 27MHz Amplifier			
		(2SC1166 Ŷ)			COILS
Q303	VS2SC495-T/-1	Transmitter, Driver (2SC495 T)			
Q304	VS2SC1237-/1F	Transmitter, Final (2SC1237)	L1	RCILC0023AFZZ	OSC. Choke
Q702 Q703	VS2SC1741//-1 VS2SC1741//-1	Voltage Regulator (2SC1741) Power Switching (2SC1741)	L101	RCILC0023AFZZ	AF Choke

REF. NO.	PART NO.	DESCRIPTION		REF. NO.	PART NO.	DESCRIPTION	
L301	RCILR0135AFZZ	Transmitter, Matching		C211	VCSATU1VF224M	.22MFD, 35V, ±20%, Tantalu	
		(Loading)		C225	VCSATU1EF105M	1MFD, 25V, ±20%, Tantalu	
L302	RCILR0055AFZZ	Transmitter, π-Filter		C228	VCEAAU1AW107Y	100MFD, 10V, +50 -10%	
L305	RCILC0011AFZZ	RF Choke (TX)		C232	VCEAAU1AW476Y	47MFD, 10V, +50 -10%	
L401	RCILR0329AFZZ	Antenna Choke		C237	VCEAAU1HW105A	1MFD, 50V, +75 -10%	
				C244	VCEAAU1AW107Y	100MFD, 10V, +50 -10%	
				C702	VCEAAU1HW105A	1MFD, 50V, +75 -10%	
	TRAN	SFORMERS		C703	VCEAAU1AW108Y	1000MFD, 10 V, $+50 - 10%$	
				C704	VCEAAU1AW476Y	47MFD, 10V, +50 -10%	
T1	RCILA0412AFZZ	Antenna		C705	VCEAAU1CW476Y	47MFD, 16V, +50 -10%	
T2	RCILR0304AFZZ	RF		C706	VCEAAU1AW477Y	470MFD, 10V, +50 -10%	
T3	RCILI0157AFZZ	1st-IF (10.695MHz)		C801	VCAAKU0XA474M	.47MFD, 6.3V, ±20%,	
T4	RCILI0157AFZZ	1st-IF (10.695MHz)				Aluminum	
T5	RCILI0228AFZZ	2nd-IF (455kHz)		C802	VCEAAU1EW335Y	3.3MFD, 25V, +50 -10%	
T6	RCILI0229AFZZ	2nd-IF (455kHz)		C803	VCEAAU1EW335Y	3.3MFD, 25V, +50 -10%	
T7	RCILI0169AFZZ	2nd-IF (455kHz)		C901	VCSATU1EF105M	1MFD, 25V, ±20%, Tantalu	
T8	RCILB0421AFZZ	2nd Local Oscillator		C903	VCEAAU1AW476Y	47MFD, 10V, +50 - 10%	
		(11.150MHz)					
Т9	RCILI0228AFZZ	2nd-IF (455kHz)					
T101	RTRNM0050AFZZ	Output and Modulation			CAP	ACITORS	
T201	RCILR3242AAZZ	Tripler (15.360MHz)					
T202	RCILB3241AAZZ	V.C.O. (Voltage Controlled Oscillator)		(Unless	otherwise specified capa	citors are 50V, +80 -20%, Ceram	ic Type.)
T203	RCILB0383AFZZ	27MHz Filter		C1	VCKZPU1HF103Z	.01MFD	
T204	RCILB0383AFZZ	27MHz Filter		C2	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80	
T205	RCILR3243AAZZ	16MHz Filter				-20%, Ceramic	
T206	RCILR3243AAZZ	16MHz Filter		C3	VCKZPU1HF103Z	.01MFD	
T301	RCILB0383AFZZ	Transmitter, 27MHz Filter		C4	VCCSPU1HL271J	270PF, 50V, ±5%, Ceramic	
T302	RCILB0383AFZZ	Transmitter, 27MHz Filter		C5	VCCSPU1HL220J	22PF, 50V, ±5%, Ceramic	
T303	RCILB0221AFZZ	Transmitter, Buffer		C6	VCKYPU1HB103M	.01MFD, 50V, ±20%, Ceramic	
T304	RCILR0037AFZZ	Transmitter, Driver		C7	VCKZPU1HF103Z	.01MFD	
T601	RTRNC0003AFZZ	Power Choke		C8	VCCSPU1HL2R0C	2PF, 50V, ±0.25PF, Ceramic	
				C9	VCCSPU1HL680J	68PF, 50V, ±5%, Ceramic	
				C10	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic	
	CR	YSTALS		C11	VCKYPU1HB103M	.01MFD, 50V, ±20%, Ceramic	
				C12	VCKZPU1HF103Z	.01MFD	-
X1	RCRSB0055AFZZ	11.150MHz		C13	VCCSPU1HL5R0C	5PF, 50V, ±0.25PF, Ceramic	
X201	RCRSB0051AFZZ	10.240MHz		C14	VCKZPU1HF103Z	.01MFD	
				C15	VCCSPU1HLR50C	0.5PF, 50V, ±0.25PF, Ceramic	
				C16	VCQYKU1HM333M	.033MFD, 50V, ±20%, Mylar	
	CERAN	MIC FILTER		C17	VCKZPU1HF103Z	.01MFD	
				C18	VCKZPU1HF103Z	.01MFD	1
CF1	RFILA0056AFZZ	455kHz		C19	VCQYKU1HM333M	.033MFD, 50V, ±20%, Mylar	
				C20	VCQYKU1HM333M	.033MFD, 50V, ±20%, Mylar	
	ELECTROLY	TIC CAPACITORS		C21	VCKYPU1HB472M	.0047MFD, 50V, ±20%, Ceramic	
				C24	VCQYKU1HM103M	.01MFD, 50V, ±20%, Mylar	
C22	VCEAAU1EW335A	3.3MFD, 25V, +75 –10%		C25	VCKZPU1HF223Z	.022MFD	
C23	VCEAAU1EW335A	3.3MFD, 25V, +75 –10%		C27	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic	
C26	VCAAKU0XA474M	.47MFD, 6.3V, ±20%,		C28	VCCSPU1HL680J	68PF, 50V, ±5%, Ceramic	
		Aluminum		C29	VCQYKU1HM333M	.033MFD, 50V, ±20%, Mylar	
C30	VCEAAU1CW106Y	10MFD, 16V, +50 - 10%		C32	VCCSPU1HL221J	220PF, 50V, ±5%, Ceramic	
C31	VCEAAU1AW227Y	220MFD, 10V, +50 -10%		C33	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic	
C37	VCEAAU1CW336Y	33MFD, 16V, +50 –10%		C34	VCCSPU1HL221J	220PF, 50V, ±5%, Ceramic	
C39	VCAAKU0XA474M	.47MFD, 6.3V, ±20%,		C35	VCKZPU1HF103Z	.01MFD	
a	rion	Aluminum		C36	VCKZPU1HF103Z	.01MFD	
C107	VCEAAU1CW106Y	10MFD, 16V, +50 –10%		C38	VCKZPU1HF103Z	.01MFD	
C108	VCEAAU1AW227Y	220MFD, 10V, +50 –10%		C101	VCKYPU1HB472M	.0047MFD, 50V, ±20%,	
C109	VCEAAU1CW106Y	10MFD, 16V, +50 –10%				Ceramic	
C112	VCEAAU1EW335A	3.3MFD, 25V, +75 -10%		C102	VCKYPU1HB472M	.0047MFD, 50V, ±20%,	
C113	VCEAAU1CW336Y	33MFD, 16V, +50 –10%		0165	110011111111111111111111111111111111111	Ceramic	
C116	VCEAAU0JW476Y	47MFD, 6.3V, +50 –10%		C103	VCQYKU1HM223M	.022MFD, 50V, ±20%, Mylar	
C117	VCEAAU1CW108Y	1000MFD, 16V, +50 –10%		C104	VCKYPU1HB222M	.0022MFD, 50V, ±20%,	
C119	VCEAAU1CW106Y	10MFD, 16V, +50 –10%	. '	,		Ceramic	, ,

REF. NO.	PART NO.	DESCRIPTION		REF. NO.	PART NO.	DESCRIPTION
C105	VCCSPU1HL271J	270PF, 50V, ±5%, Ceramic		C309	VCKZPU1HF103Z	.01MFD
C106	VCQYKU1HM683M	.068MFD, 50V, ±20%, Mylar		C310	VCCCPU1HH100F	10PF (CH), 50V, ±1PF,
C110	VCCSPU1HL470J	47PF, 50V, ±5%, Ceramic				Ceramic
C111	VCQYKU1HM104M	.1MFD, 50V, ±20%, Mylar		C311	VCKZPU1HF103Z	.01MFD
C114	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80		C312	VCCSPU1HL221J	220PF, 50V, ±5%, Ceramic
		-20%, Ceramic		C313	VCCSPU1HL471J	470PF, 50V, ±5%, Ceramic
C118	VCQYKU1HM333M	.033MFD, 50V, ±20%, Mylar		C314	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80
C120	RC·KZ1010AFZZ	1000PF, 50V, +80 –20%,				-20%, Ceramic
C122	VCCCDUITH COOL	Ceramic (Wage Type)		C316	VCCSPU1HL180J	18PF, 50V, ±5%, Ceramic
C122 C123	VCCSPU1HL680J VCCSPU1HL680J	68PF, 50V, ±5%, Ceramic		C317	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80
C123	VCKYPU1SD103Z	68PF, 50V, ±5%, Ceramic .01MFD (Z5T), 30V, +80		C318	VCKYPU1SD103Z	-20%, Ceramic .01MFD (Z5T), 30V, +80
C124	VCKTFCT3DT03Z	-20%, Ceramic		C316	VCKTPUISDIUSZ	-20%, Ceramic
C125	VCKZPU1HF103Z	.01MFD		C319	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80
C201	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80				-20%, Ceramic
		-20%, Ceramic		C320	VCCSPU1HL511J	510PF, 50V, ±5%, Ceramic
C202	VCCCPU1HH330J	33PF (CH), 50V, ±5%, Ceramic		C321	VCCSPU1HL331J	330PF, 50V, ±5%, Ceramic
C203	RTO-H1009AFZZ	Trimmer Capacitor, 10.240MHz		C322	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80
		Oscillator				-20%, Ceramic
C204	VCCSPU1HL391J	390PF, 50V, ±5%, Ceramic		C323	VCCSPU1HL471J	470PF, 50V, ±5%, Ceramic
C205	VCCSPU1HL151J	150PF, 50V, ±5%, Ceramic		C324	VCCSPU1HL271J	270PF, 50V, ±5%, Ceramic
C206	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic		C325	V.CCSPU1HL391J	390PF, 50V, ±5%, Ceramic
C207	VCQYKU1HM223M	.022MFD, 50V, ±20%, Mylar		C326	VCCSPU1HL150J	15PF, 50V, ±5%, Ceramic
C208 C209	VCCCPU1HH150J VCCCPU1HH5R0C	15PF (CH), 50V, ±5%, Ceramic		C330	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80
C209	VCCCPUTHHSRUC	5PF (CH), 50V, ±0.25PF, Ceramic		C221	VCV2BUUUE1022	−20%, €eramic
C210	VCCSPU1HL3R0C	3PF, 50V, ±0.25PF, Ceramic		C331 C333	VCKZPU1HF103Z VCCSPU1HL511J	.01MFD
C212	VCCCPU1HH470J	47PF (CH), 50V, ±5%, Ceramic		C334	VCKZPU1HF103Z	510PF, 50V, ±5%, Ceramic .01MFD
C213	VCCUPU1HJ100J	10PF (UJ), 50V, ±5%, Ceramic		C335	VCCSPU1HL511J	510PF, 50V, ±5%, Ceramic
C214	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic		C336	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic
C215	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic		C338	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic
C216	VCCUPU1HJ180J	18PF (UJ), 50V, ±5%, Ceramic		C401	VCCSPU1HL151J	150PF, 50V, ±5%, Ceramic
C217	VCQYKU1HM223M	.022MFD, 50V, ±20%, Mylar		C402	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80
C218	VCKYPU1HB102M	.001MFD, 50V, ±20%, Ceramic				-20%, Ceramic
C219	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic		C601	VCKZPU1HF103Z	.01MFD
C220	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic		C604	VCKZPU1HF333P	.033MFD, 50V, +100 −0%,
C221	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic		0405		Ceramic
C222 C223	VCKYPU1HB102M	.001MFD, 50V, ±20%, Ceramic		C605	VCKZPU1HF333P	.033MFD, 50V, +100 –0%,
C223	VCQYKU1HM223M VCCSPU1HL101J	.022MFD, 50V, ±20%, Mylar 100PF, 50V, ±5%, Ceramic		C606,		Ceramic
C224	VCCCPU1HH330J	33PF (CH), 50V, ±5%, Ceramic		C607	RC-KZ1009AFZZ	Feed Through Capacitors with Bracket
C227	VCKZPU1HF103Z	.01MFD		C608	VCKZPU1HF103Z	.01MFD
C229	VCKZPU1HF103Z	.01MFD		C701	VCQYKU1HM333M	.033MFD, 50V, ±20%, Mylar
C230	VCCCPU1HH100F	10PF (CH), 50V, ±1PF,		C710	RC-KZ1010AFZZ	
		Ceramic		C711	RC-KZ1010AFZZ	
C231	VCKZPU1HF103Z	.01MFD		C712	RC-KZ1010AFZZ	
C233	VCCRPU1HH390J	39PF (RH), 50V, ±5%, Ceramic		C715	RC-KZ1010AFZZ	
C234	VCKZPU1HF103Z	.01MFD		C717	RC-KZ1010AFZZ	
C235	VCCRPU1HH330J	33PF (RH), 50V, ±5%, Ceramic		C718	RC-KZ1010AFZZ	
C236	VCKZPU1HF103Z	.01MFD		C719	RC-KZ1010AFZZ	
C238 C239	VCKYPU1HB102M VCCSPU1HL820J	.001MFD, 50V, ±20%, Ceramic 82PF, 50V, ±5%, Ceramic		C721	RC-KZ1010AFZZ	
C240	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic		C722	RC-KZ1010AFZZ	1000PF, 50V, +80 –20%,
C241	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic		C723 C724	RC-KZ1010AFZZ	Ceramic (Wage Type)
C243	VCCSPU1HL151J	150PF, 50V, ±5%, Ceramic		C725	RC-KZ1010AFZZ RC-KZ1010AFZZ	
C245	VCQYKU1HM103M	.01MFD, 50V, ±20%, Mylar		C726	RC-KZ1010AFZZ	
C301	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic		C727	RC-KZ1010AFZZ	
C302	VCCSPU1HL390J	39PF, 50V, ±5%, Ceramic		C728	RC-KZ1010AFZZ	
C303	VCCSPU1HL390J	39PF, 50V, ±5%, Ceramic		C729	RC-KZ1010AFZZ	
C304	VCCSPU1HL3R0C	3PF, 50V, ±0.25PF, Ceramic		C730	RC-KZ1010AFZZ	
C305	VCKZPU1HF103Z	.01MFD		C731	RC-KZ1010AFZZ	
C306	VCKZPU1HF103Z	.01MFD		C902	VCTYPU1EX103M	.01MFD, 25V, ±20%, Ceramic
C307 C308	VCCSPU1HL151J	150PF, 50V, ±5%, Ceramic				
C308	VCCSPU1HL4R0C	4PF, 50V, ±0.25PF, Ceramic				1

REF. NO.	PART NO.	DESCRIPTION	REF. NO.	PART NO.	DESCRIPTION	
	RESIST	TORS ARRAY	R140	VRD-ST2HA1R0K	1 ohm, 1/2W, ±10%, Carbon	
			R201	VRD-SS2EY563J	56K ohm	
RM901	RMPTC1002AAZZ	470 ohm × 3, 1/8W	R202	VRD-SS2EY473J	47K ohm	
A ~ C		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	R203	VRD-SS2EY152J	1.5K ohm	
RM902		470 ohm x 4, 1/8W	R205	VRD-SS2EY222J	2.2K ohm	
A ~ D			R206 R207	VRD-SS2EY103J	10K ohm	
RM903 A ∼ E	RMPTC0004AFZZ	470 ohm × 5, 1/4W	R207	VRD-SS2EY562J VRD-SS2EY103J	5.6K ohm 10K ohm	
RM904	4-)		R209	VRD-SS2EY224J	220K ohm	
A ∼ F	RMPTC1003AAZZ	1.5K ohm × 6, 1/8W	R210	VRD-SS2EY103J	10K ohm	
			R211	VRD-SS2EY102J	1K ohm	
			R212	VRD-SS2EY103J	10K ohm	
	RI	ESISTORS	R214	VRD-SS2EY561J	560 ohm	
(Unless	otherwise specified resis	tors are 1/4W, ±5%, Carbon Type).	R215	VRD-SS2EY222J	2.2K ohm	
			R216	VRD-SS2EY331J	330 ohm	
R1	VRD-ST2EE472J	4.7K ohm	R217	VRD-SS2EY683J	68K ohm	
R2	VRD-ST2EE152J	1.5 K ohm	R219	VRD-SS2EY560J	56 ohm	
R3 R4	VRD-ST2EE102J VRD-ST2EE222J	1K ohm	R220 R221	VRD-SS2EY471J VRD-SS2EY183J	470 ohm 18K ohm	
R4 R5	VRD-ST2EE222J VRD-ST2EE473J	2.2K ohm 47K ohm	R221	VRD-SS2EY333J	33K ohm	
R6	VRD-ST2EE562J	5.6K ohm	R223	VRD-SS2EY102J	1K ohm	
R7	VRD-ST2EE471J	470 ohm	R224	VRD-SS2EY680J	68 ohm	
R8	VRD-ST2EE4713	4.7K ohm	R225	VRD-SS2EY561J	560 ohm	
R9	VRD-ST2EE333J	33K ohm	R226	VRD-SS2EY471J	470 ohm	
R10	VRD-ST2EE681J	680 ohm	R227	VRD-SS2EY560J	56 ohm	
R12	VRD-SU2EY223J	22K ohm	R228	VRD-SS2EY222J	2.2K ohm	
R13	VRD-ST2EE472J	4.7K ohm	R229	VRD-SS2EY222J	2.2K ohm	
R14	VRD-ST2EE102J	1K ohm	R 301	VRD-ST2EE123J	12K ohm	
R15	VRD-ST2EE273J	27K ohm	R 302	VRD-ST2EE222J	2.2K ohm	
R16	VRD-SU2EY562J	5.6K ohm	R303	VRD-ST2EE221J	220 ohm	
R17	VRD-SU2EY102J	1K ohm	R304	VRD-ST2EE223J	22K ohm 47 ohm	
R18	VRD-SU2EY102J	1K ohm	R305 R306	VRD-ST2EE470J VRD-ST2EE332J	3.3K ohm	
R20 R21	VRD-SU2EY224J	220K ohm 33K ohm	R307	VRD-ST2EE3323	100 ohm	
R22	VRD-SU2EY333J VRD-SU2EY223J	22K ohm	R308	VRD-ST2EE101J	100 ohm	
R23	VRD-SC2E12233 VRD-ST2EE333J	33K ohm	R309	VRD-ST2EE680J	68 ohm	
R24	VRD-ST2EE153J	15K ohm	R310	VRD-ST2HA220J	22 ohm, 1/2W, ±5%, Carbon	
R27	VRD-SU2EY104J	100K ohm	R312	VRD-ST2HA471J	470 ohm, 1/2W, ±5%, Carbon	
R28	VRD-SU2EY683J	68K ohm	R313	VRD-ST2EE332J	3.3K ohm	
R29	VRD-SU2EY104J	100K ohm	R516	VRD-ST2HA101K	100 ohm, 1/2W, ±10%, Carbon	
R 30	VRD-SU2BY333J	33K ohm, 1/8W, ±5%, Carbon	R517	VRD-ST2HA681K	680 ohm, 1/2W, ±10%, Carbon	
R31	VRD-SU2EY154J	150K ohm	R518	VRS-PT3DB221K	220 ohm, 2W, ±10%, Oxide	
R34	VRD-ST2EE222J	2.2K ohm			Film	
R35	VRD-SU2EY222J	2.2K ohm	R702	VRD-ST2EE102J	1K ohm	
R36	VRD-ST2EE223J	22K ohm	R706	VRD-ST2EE104J	100K ohm	
R 37 R 38	VRD-ST2EE472J	4.7K ohm	R707	VRD-ST2EE104J	100K ohm	
R39	VRD-ST2EE151J VRD-ST2EE101J	150 ohm 100 ohm	R710 R711	VRD-ST2EE101J VRD-ST2EE471J	100 ohm 470 ohm	
R40	VRD-SU2EY823J	82K ohm	R712	VRD-ST2EE4713	4.7K ohm	
R41	VRD-SC2E 18233 VRD-ST2EE471J	470 ohm	R713	VRD-ST2EE331J	330 ohm	
R42	VRD-ST2EE563J	56K ohm	R801	VRS-PT3DB470K	47 ohm, 2W, ±10%, Oxide	
R43	RVR-M0146AFZZ	30K (B) ohm, Deepest Point			Film	
		Adjust	R802	VRD-ST2EY102J	1K ohm	
R44	VRD-SU2EY683J	68K ohm	R803	VRD-ST2EY104J	100K ohm	
R45	VRD-SU2EY682J	6.8K ohm	R804	VRD-ST2EY102J	1K ohm	
R102	VRD-ST2EE222J	2.2K ohm	R805	VRD-ST2EY331J	330 ohm	
R103	VRD-ST2EE470J	47 ohm	R806	VRD-ST2EY104J	100K ohm	
R106	VRD-ST2EE222J	2.2K ohm	R901	VRD-SC2EF471J	470 ohm	
R107	VRD-ST2EE222J	2.2K ohm	R902	VRD-SU2EF471J	470 ohm	
R108	VRD-ST2EE223J	22K ohm	R921	VRD-SC2EES611	470K ohm	
R109	VRD-ST2EE153J	15K ohm	R923 R924	VRD-SC2EF561J RVR-C0001AAZZ	560 ohm	
R110	RVR-M0123AFZZ	l K (B) ohm, Modulation Level Adjust	R925/ 1		5K (C) ohm, Squelch Control 5K (B) ohm, Volume Control	
D112	VRD-SU2EY563J	56K ohm	SW903	RVR-B0006AAZZ	with OFF-ON Switch	
R112						

	EF. PART NO.	DESCRIPTION	REF. NO.	PART NO.	DESCRIPTION
	MISCELLANEOUS			PGUMS0110AF00	Cushion, P.L.L. Unit, Rubber
				PHAG-001MAFFC	Microphone Hanger, Small
01	GCABA1465AFSA	Cabinet, Speaker Side		PHAG-002MAFFN	Microphone Hanger, Large
02	GCABB1465AFSA	Cabinet, Punching Metal Side		PRDAR0144AFFW	Heat Sink, Transistors (Q303
03	GCABA3464AFSA	Cabinet, Main Unit			and Q304)
04	GCABB3464AFFW		1	PRDAR0145AFFW	Heat Sink, Integrated Circuit
05	HINDM1080AFSB	Emblem "SHARP", Cabinet of Punching Metal Side		PSLDM3136AFFW	IC101 Shield Plate, Wage Type Cerami
06	HINDM1182AFZZ	Indication Plate, Spec.		PSLDM3130AFFW	Capacitors, Remote P.C.
24	HPNC-0109AFSA	Punching Metal, Speaker Unit			Board
07	HPNLH0013AAZZ	Cabinet, Front, Microphone Unit		PSPO-0059AFZZ	Sponge, Speaker Unit P.C. Board
08	HPNLH0010AAZZ	Cabinet, Rear, Microphone Unit	CNP101 CNP102		Connecting Cord with Sockets
09	JKNBZ0001AAZZ	Knob, Up and Down (Channel	CNP103	OCNW-0260AE77	(10-Pin, 14-Pin) and Plugs
10	JKNBZ0002AAZZ	Selection Switch) Knob, Off-On Switch/Volume	CNP104 CNS702	,	$(7-Pin, 10-Pin \times 2, 5-Pin)$ Assembly
	***************************************	Control, Squelch Control	CNS704		
11	JKNBZ0003AAZZ	Knob, Press-to-Talk Switch	1	QCNCM0902AGZZ	Plug, 9-Pin
	LANGF0407AFFW	Metal Plate, Output/Modulation		QCNCM0902AGZZ	Plug, 9-Pin
		Transformer, Small (Flat)	1	QCNCM1101AGZZ	Plug, 11-Pin
	LANGQ0545AFFW			QCNCM1301AGZZ	Plug, 13-Pin
		Board	I	QCNTZ0071AFZZ	Plug, 1-Pin
	LANGR0418AFFW	C ,		QCNCM095BAFZZ	Plug, 2-Pin, Speaker
		tion Transformer, Large		QCNCM1401AGZZ	Plug, 14-Pin
	LANGT0650AFFW	Bracket, Cabinet of Punching	CNS701		Remote Control Cable with
		Metal Side	CNS703	, QCNW-0258AFZZ	Plug (PG801) and Sockets
	LANGT0651AFFW	Mounting Bracket, Main Unit	PG801/	ZCITII OZJONI ZL	Assembly
	LANGT0663AFZZ	Fixing Metal, Punching Metal	18	1	
12	LBRC-0053AFSA	Mounting Bracket Assembly, Speaker Unit	CNS801	QCNW-0256AFZZ	Connecting Cord with Socket, Speaker
	LBSHZ0051AFZZ	Bushing, Transistor Q304	CNS803	,	
	LCHSM0264AFFW	Chassis, Main Unit	SO801,	OCNIW 0255 A DZZ	Microphone Cable with Sockets
	LCHSM0279AFFW	Chassis, Speaker Unit	SO901/	QCNW-0255AFZZ	and Bushing Assembly
	LCHSM2082AAZZ	Bracket, P.L.L. Circuit P.C.	19		
		Board		QFS-D201AAGNA	Fuse, 200mA
13	LCHSS0126AFFW	Chassis, Front, Main Unit		QFS-A232AAFNA	Fuse, 2.3A
	LHLDW3009AFFW			QFSHJ9052AFZZ	Power Supply Cord with Fuse
14	LHLDW3057AFFW			QFSHJ9054AFZZ	Holder and Socket Memory Cord (Power Supply)
15	LX-BZ0237AFFB	Screw, Cabinet, Main Unit			with Fuse Holder and Plug
16	LX-BZ0248AFZZ	Moulding Screw, $5\phi \times 15$ mm	J101/)	OLLULOOSS	
	LX-LZ0001AGZZ	Rivet, Indication Plate of Spec.	20	QJAKA0052AFZZ	Jack, External Speaker
	LX-LZ0051AF00	Push Rivet, Nylon, Speaker Unit		QPWBE0061AAZZ	Printed Circuit Board, Remote Microphone Circuit
17	LX-NZ0123AFFN	Nut, Remote Control Socket (24¢)		QPWBF0052AAZZ	Printed Circuit Board, P.L.L. Circuit
	LX-TZ0001AFFE	Self-Tapping Screw (5φ), Mounting Bracket of Main		QPWBF0612AFZZ	Printed Circuit Board, Main Circuit
		Unit		QPWBF0675AFZZ	Printed Circuit Board, Remote
	LX-WZ3017CEFN	Shakeproof Lockwasher Exter-			Unit
	LX-WZ3057AFFN	nal Type, Main P.C. Board Washer, Remote Control		QPWBF0676AFZZ	Printed Circuit Board, Speaker Unit
		Socket (24 ϕ)	PG201	QPLGZ0850AFZZ	Plug, 8-Pin
	LX-WZ9055AFZZ	Washer (ϕ 6.5), Speaker Unit,	PG202	QPLGZ0850AFZZ	Plug, 8-Pin
	PCOVS1002AAZZ	Plastic Cover, P.L.L. Circuit P.C.	PG601/ 21	QSOCZ2454AFZZ	Plug, Power Supply
	PFLT-0326AF00	Board Felt, Cabinet of Punching	PG901	QCNTZ0001AAZZ QSOCE0401AFZZ	Plug, 20-Pin, Microphone Unit Socket, Test Point TP308 and
		Metal Side, 90mm x 90mm			TP309
	PFLT-0338AF00	Felt, Cabinet of Punching Metal Side, 34mm x 5mm		QPLGE0403AGZZ	Plug, Test Point TP308 and TP309
			SO401/		

REF. NO.	PART NO.	DESCRIPTION	REF. NO.	PART NO.	DESCRIPTION
NO. SO701/ 23 SW101-A D/ RLY601 SW701, RLY701 SW901 SW902 SW903/ R925 SW904	QCNW-0254AFZZ	Memory Cord with Socket Relay with Receiver/Transmitter Switch Relay with Power Switch Switch, Up (Channel Selection) Switch, Down (Channel Selection) Switch, OFF-ON with Volume Control (5K-B ohm) Switch, Press-to-Talk (P.T.T.)	NO.	RTUNS0052AFZZ SPAKA0488AFZZ SPAKC1061AFZZ SPAKX0162AFZZ SSAKH0016AGZZ SSAKH0070AGZZ SSAKH0113AGZZ TINSE0529AFZZ VSP0080P-288A XCBSC30P10000	P.L.L. Synthesizer Unit Packing Add. Packing Case Packing Add., Cushion Polyethylene Bag, Main Unit Polyethylene Bag, Remote Control Microphone Unit Polyethylene Bag, Speaker Unit Operation Manual Speaker, 8 ohms, 8cm Screw, Mounting Bracket of Speaker Unit Tapping Screw (3\$\phi\$ x 12mm),
	RMICD0001AAZZ RMICB0051AFZZ	Dynamic Microphone, 500 ohms (at 1kHz) Remote Control Microphone		XWHGZ28-20100	Microphone Unit Rubber Washer, Main P.C. Board
		Unit			