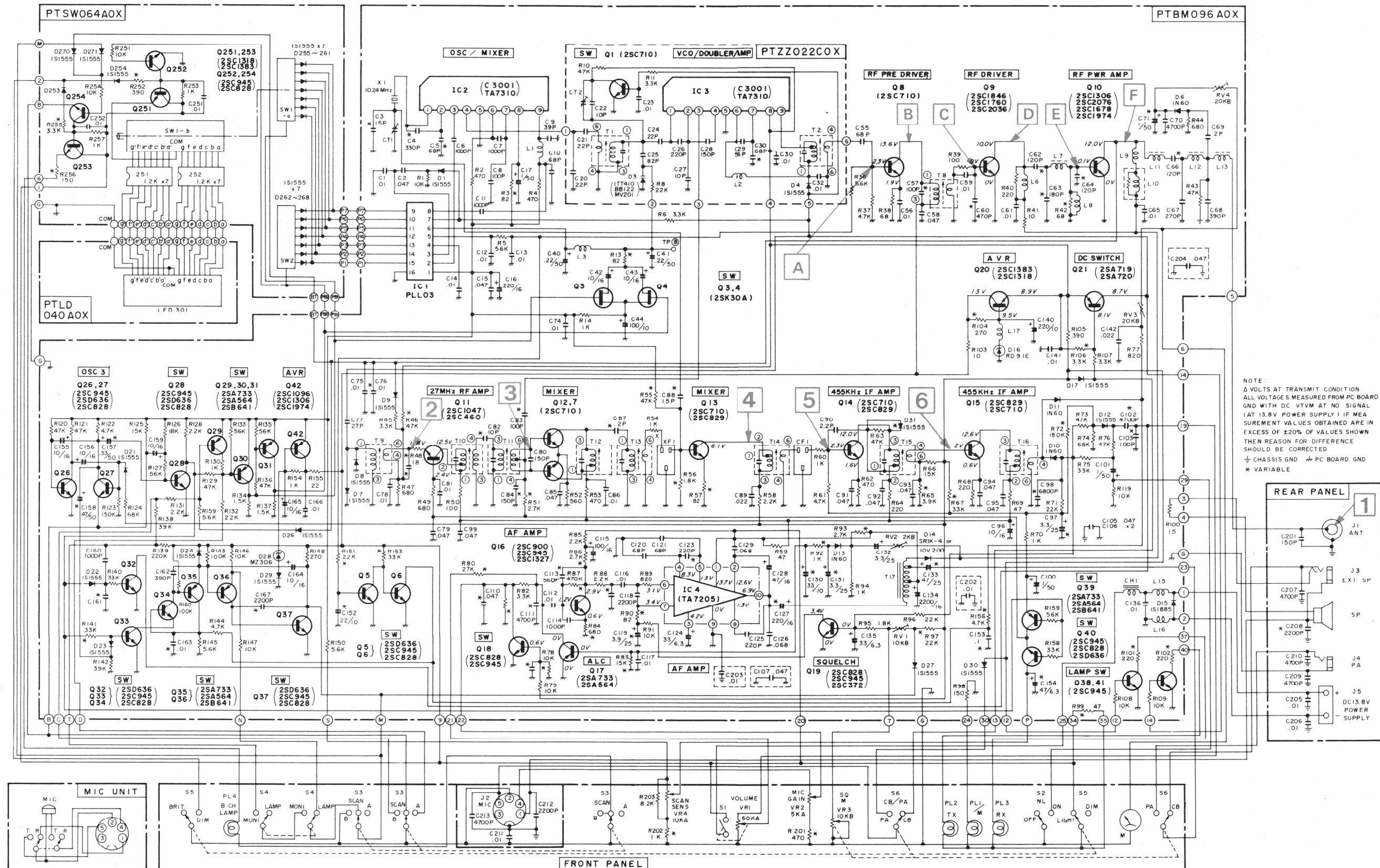
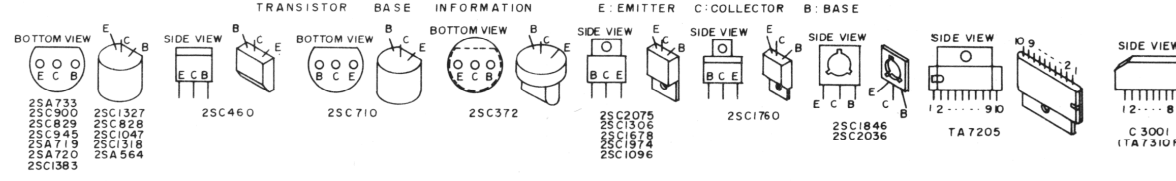
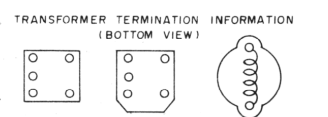


SECTION 7 SCHEMATIC DIAGRAM FOR LCM-8



NOTE:
 Δ VOLTS AT TRANSMIT CONDITION
 ALL VOLTAGES MEASURED FROM PC BOARD
 GND WITH DC VTVM AT NO SIGNAL
 (AT 13.8V POWER SUPPLY) IF MEASUREMENT
 VALUES OBTAINED ARE IN
 EXCESS OF ±20% OF VALUES SHOWN
 THEN REASON FOR DIFFERENCE
 SHOULD BE CORRECTED
 ⊕ CHASSIS GND ⊕ PC BOARD GND
 * VARIABLE



** Alphabetical numbers in boxes on schematic diagram are TX waveform check points.
 ** Numerical numbers in boxes on schematic diagram are RX injection voltage check points.