

CORRECTION
Completed
7/6 out 8/4

RE: GRANT SLIDE - Vol. 19, page 35-36.

O.K. - Problem cropped up on this one: DELETE STEP 5 V-19, pg.35.

Once over look on complete schematic found the problem with changing the value. Is linearizing resistor for the Clarifier pot. Leave the original resistor in place. Cross step 5 out in your book!

This will solve any problems with modification, as the rest is correct.

GRANT SLIDE - (+3.5KHz. -10KHz) - Tested!
by D.G.

*Use drawing on page 36 of Vol. 19 for reference, if needed.

1. Gray and Blue wires on clarifier pot removed at small PCB.
2. Blue to PCB Gnd., Gray to 8.4VDC common trace at front of main PCB.
3. Remove completely D40, D44, and D45.
4. Place a solid wire jumper between D40 and D44 cathode etch lands.
5. Replace either D40 or D44 with: 1-Super Diode and both choke coils that come with the Grant Slide Kit, wired in series.
6. Counter to TP3, Band-Mid, Mode-USB, Clarifier to 12 o'clock pos., Ch. Sel to 19.
7. Adj I26 for 16.4925MHz or highest possible reading. (Will probably not reach) - bring clarifier up until counter does read 16.4925MHz. Don't move the clarifier until alignment finished.
8. Adj I25 for 16.4900MHz in AM Mode.
9. Adj I27 for 16.4875MHz in LSB Mode.
10. Band to Low, Mode to AM, - adjust I22 for 16.040MHz.
11. Adj I23 for 16.0425MHz in USB Mode.
12. Adj I24 for 16.0375MHz in LSB Mode.
13. Center Fo on all bands should now be at the same point. The only 'problem' if it is - clarifier is usually at 3 o'clock position for center Fo.

NOTE: Actual slide was; (AM, +3.5, -12.0)(USB, +3.0, -10.0) and (LSB, +3.8, -13.5).