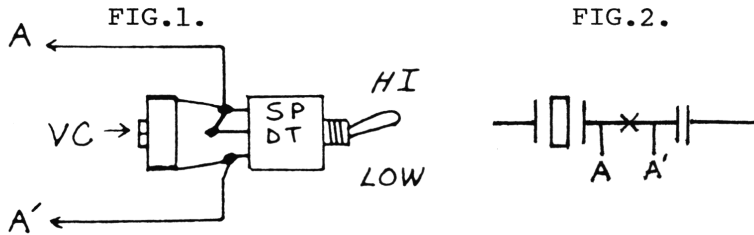


REALISTIC TRC-426

5KC OFFSET



1. Wire up the SPDT switch and trim capacitor as shown in Fig.1.
2. Unsolder and lift the collector of Q804 and at the same point lift the leg of C809.
3. Cut the foil trace between the 10.240MHz. crystal and C802 as shown in Fig.2.
4. Solder the wires from the switch across the cut trace.
5. With the switch in low position, adjust VC for 27.410 on Ch.40.
6. Switch to the high position and check for 27.405. If necessary, adjust CT801 to obtain this reading.

CHANNEL CONVERSION

1. Locate, unsolder, and remove R808 (off of pin 8 of the TC9106P PLL chip.)
2. Solder one leg of the 4700ohm resistor provided to pin 8 of PLL chip.
3. Run a wire from the other leg of the resistor to terminal Q on the DPDT switch supplied.
4. Run a wire from terminal P on the switch to where the other leg of R808 was connected, Also run a wire from terminal P to the red dot post of the epoxy pak.
5. Run a wire from terminal S on the switch to pin 1 of the PLL chip.
6. Remove the jumper between C807 & C805.
7. Replace C807 with the 47pf capacitor provided.
8. Run a wire from C807 to terminal K on the switch.
9. Run a wire from C805 to terminal J on the switch.
10. Run a wire from terminal L on the switch to the yellow dot post of the epoxy pak.
11. Run a wire from the unmarked terminal on the epoxy pak to ground.

Now this unit will operate on Channels 42-86, 1-40 and on half channels 1A-40A.

