

REALISTIC 421

5KC OFFSET

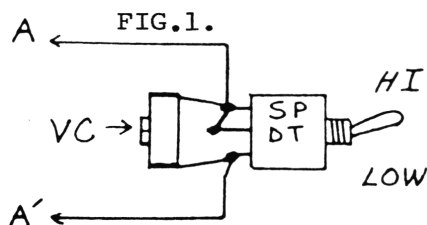
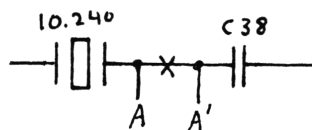


FIG. 2.



1. Wire up the SPDT switch and trim capacitor as shown in Fig.1.
2. Cut the foil trace between the 10.240MHz. crystal and C38 as shown.
3. Solder the wires from the switch across the cut trace.
4. With the switch in the low position, adjust VC for 27.410 on Ch.40.
5. Switch to the high position and check for 27.405 Adjust CT if necessary to obtain this reading.

CHANNEL CONVERSION

1. Isolate pin 9 of the PLL chip by cutting the foil trace.
2. Solder one leg of the 4700ohm resistor supplied to pin 9 of the PLL chip.
3. Run a wire from the other leg of the resistor to terminal Q on the DPDT switch provided.
4. Run a wire from terminal P on the switch to the other side of the trace (anode of D7). Also run a wire from terminal P to the unmarked post of the epoxy pak.
5. Run a wire from terminal S on the switch to ground.
6. Locate, unsolder, and remove C40.
7. Solder one leg of the 47pf capacitor provided to the hole opposite C41.
8. Run a wire from the other leg of the 47pf capacitor to terminal K on the switch.
9. Run a wire from terminal J on the switch to where C40 was connected to C41.
10. Run a wire from terminal L on the switch to the yellow dot post of the epoxy pak.
11. Run a wire from the red dot post of the epoxy pak to pin 11 of the PLL chip.

Now this unit will operate on Channels 42-86, 1-40 and on half channels 1A-40A.

