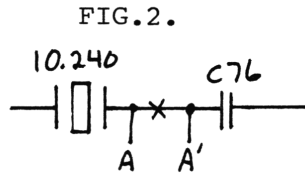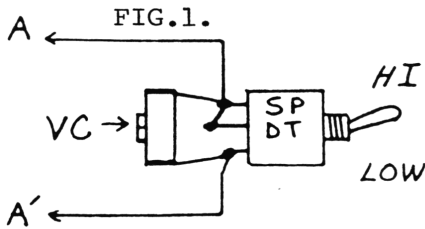# REALISTIC 410

5KC OFFSET   FIG.1.   FIG.2.

1. Wire up the SPDT switch and trim capacitor as shown in Fig.1.
2. Cut the foil trace between the 10.240MHz. crystal and C76 as shown in Fig.2.
3. Solder the wires from the switch to each side of the cut trace.
4. With the switch in the low position, adjust VC for 27.410 on Ch.40.
5. Switch to the high position and check for 27.405. If necessary, alter the value of C76 to obtain this reading.

CHANNEL CONVERSION

1. Isolate pin 20 of the PLL chip by cutting the foil trace.
2. Solder one leg of the 4700ohm resistor supplied to pin 20 of the PLL chip.
3. Run a wire from the other leg of the resistor to terminal Q on the DPDT switch supplied.
4. Run a wire from terminal P on the switch to the other side of the cut trace (anode of D10). Also run a wire from terminal P to the unmarked post of the epoxy pak.
5. Run a wire from terminal S on the switch to ground.
6. Unsolder and lift the leg of C66 connected to CF3/R109.
7. Run a wire from terminal K on the switch to the lifted leg of C66.
8. Run a wire from terminal J on the switch to where C66 was connected.
9. Run a wire from terminal L on the switch to the yellow dot post of the epoxy pak.
10. Run a wire from the red dot post of the epoxy pak to pin 18 of the PLL chip.

   Now this unit will operate on channels 42-86, 1-40 and on half channels 1A-40A.