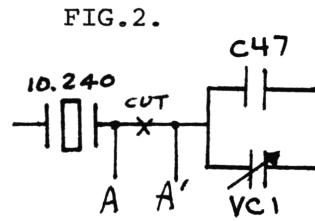
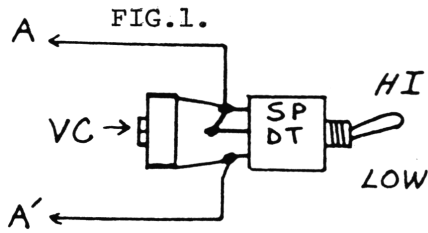


MIDLAND 77-824B & REALISTIC TRC-427

5KC OFFSET



1. Wire up the SPDT switch and trim cap as shown in Fig.1.
2. Cut the foil trace between the 10.240 MHz. crystal and VC1/C47, Fig.2.
3. Solder the wires from the switch to each side of the cut trace.
4. With switch in low position, adjust VC for 27.410 on CH. 40.
5. Switch to high position and check for 27.405, adjust VC1 if necessary to obtain this reading.

CHANNEL CONVERSION

1. Unsolder and lift the leg of R71 opposite pin 8 of the TC9106P PLL chip.
2. Run a wire from terminal Q on the DPDT switch provided to lifted leg of R71.
3. Run a wire from terminal P on the switch to where R71 was connected. Also, run a wire from Terminal P on the switch to the red dot post of the epoxy pak.
4. Run a wire from terminal S on the switch to pin 1 of the TC9106P chip.
5. Unsolder and remove C113 (just off pin 4 of the TA7310 VCO/Mixer chip)
6. Solder one leg of the 47pf capacitor supplied to pin 4 of the TA7310 chip.
7. Run a wire from the other leg to terminal K on the switch.
8. Run a wire from terminal J on the switch to where other end of C113 was connected.
9. Run a wire from terminal L on the switch to the yellow dot post of the epoxy pak.
10. Run a wire from the unmarked terminal of the epoxy pak to ground.

Now this unit will operate on channels 42-86, 1-40 and on half channels 1A-40A.

