

PLL PINOUT DIAGRAMS (CONT)

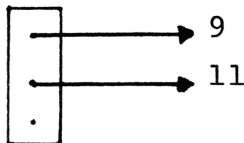
03A Chip-

1. V+
2. Qin (10.240)
3. LD1 Kills output if channel select is improperly positioned between channels.
4. LD3 Loop Lock detector output
5. LD2 Inverter
6. Do \emptyset detector output
7. TIR
8. Fin Prog. div. input \leq 7MHz
9. P6
10. P5
11. P4
12. P3
13. P2
14. P1
15. P0
16. Ground

Programming inputs to code connection

Extra Channels-

Replace 03A with 02A
(rewiring necessary)
for extra channels.



1-9 26.905-26.805
10-22 27.435-27.585