

PALOMAR SSB-500 UPDATE

CPI CP300, 400 UPDATE

1. Palomar SSB-500 (MC145106 PLL) - additional information:
 - a. Ground Pin 6 (PLL chip) for divide by 5 through a switch.
 - b. +5-6VDC to Pin 6 (PLL) for divide by 10 through a switch.

NOTE: We suggest that you use one switch for this, such as a miniature SPDT center off toggle or DPDT center off.

2. CPI CP300, 400 (with 79 channel ROM installed), for channels 80-99 - sequence starts on channel 30:
 - a. Ground Pin 3 of ICJ through a switch. This will give you 80-99 starting on channel 30.
 - . NOTE: This will work on the CP2000 base with the original PLL! VCO must be retuned.
 - b. For #10KHz jump - to get RC channels (300 & 400)
 1. Through a switch ground Pin 4 of ICG.
 2. This function must be combined with -5 KHz drop (+5V to TP10) by putting on a SPDT center off miniature toggle switch in hole below 10's selector switch (mobiles only (300 & 400)).
 - c. For low frequencies on CPI's.
 1. Run +5 volts through a switch to Pin 4 of ICJ
 2. Make sure VCO is retuned so that these frequencies will fire.
 3. It is doubtful that the VCO will cover both high and low frequencies at once. Changing the two VCO capacitors to 300pF each will help.

MIDLAND 7001

Change the 10.46670 crystal to:

Hi freq.: Ch. 41-85 - 10.6916 xtal.

Lo freq.: Ch. 26.955 - 26.515 change to 10.2416 xtal.

Slide Modification:

Clip D49, R148, jump D50

Install Super Diode (available from Secret CB) in place of D4.

Jump C27 with 22pF capacitor.

Run a wire from unused terminal on clarifier to 9V source - Emitter of Q36.

If you unground Pin 9 you will get even frequencies. With a 10.46670 crystal, the set goes low when Pin 9 is ungrounded, from 26.510 to 26.950.