



CHANNEL EXPANSION FOR REALISTIC TRC 431

1. Remove the bottom cover and expose the P/C board. There is a metal cover over the PLL. Bind up the tabs and remove. This will be reinstalled after the modification is completed.
2. Using an exacto knife, carefully isolate Pin 7 of the PLL chip IC801 by cutting around the pin, then bridge the cut with a 4.7K $\frac{1}{4}$ W resistor. The 4.7K resistor allows the pin to go to its normal state when switch voltage is not applied.
3. Construct a switch kit using a DPDT center off subminiature toggle switch.
4. Check fo output on all channels. Adjust T802 or T803 if necessary. Insert 1000Hz and adjust for maximum power out with a peak reading meter.
5. Adjust VR7 for ALC maximum modulation. Adjust L901, L902, L903, L904, L905, L907, L910. Do not adjust L901. This is the 54 MHz trap and will result in TVI.

POSITION 1

1.	26.645	11.	26.765	21.	26.895	31.	26.675
2.	26.655	12.	26.785	22.	26.905	32.	26.685
3.	26.665	13.	26.795	23.	26.615	33.	26.695
4.	26.685	14.	26.805	24.	26.915	34.	26.705
5.	26.695	15.	26.815	25.	26.605	35.	26.715
6.	26.705	16.	26.835	26.	26.625	36.	26.725
7.	26.715	17.	26.845	27.	26.635	37.	26.735
8.	26.735	18.	26.855	28.	26.645	38.	26.745
9.	26.745	19.	26.865	29.	26.655	39.	26.755
10.	26.755	20.	26.885	30.	26.665	40.	26.765

CENTER NORMAL

POSITION 2

1.	27.285	11.	27.405	21.	27.535	31.	27.315
2.	27.295	12.	27.425	22.	27.545	32.	27.325
3.	27.305	13.	27.435	23.	27.255	33.	27.335
4.	27.325	14.	27.445	24.	27.555	34.	27.345
5.	27.335	15.	27.455	25.	27.245	35.	27.355
6.	27.345	16.	27.475	26.	27.265	36.	27.365
7.	27.355	17.	27.485	27.	27.275	37.	27.375
8.	27.375	18.	27.495	28.	27.285	38.	27.385
9.	27.385	19.	27.505	29.	27.295	39.	27.395
10.	27.395	20.	27.595	30.	27.305	40.	27.405