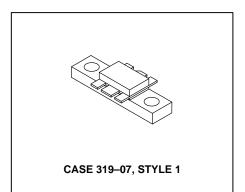
The RF Line NPN Silicon RF Power Transistor

... designed for 12.5 volt UHF large–signal, **common–base** amplifier applications in industrial and commercial FM equipment operating in the range of 806–960 MHz.

- Specified 12.5 Volt, 870 MHz Characteristics
 Output Power = 45 Watts
 Power Gain = 4.5 dB Min
 Efficiency = 60% Min
- Series Equivalent Large-Signal Characterization
- Internally Matched Input for Broadband Operation
- Tested for Load Mismatch Stress at All Phase Angles with 10:1 VSWR @ High Line and Rated Drive
- Gold Metallized, Emitter Ballasted for Long Life and Resistance to Metal Migration
- · Silicon Nitride Passivated

MRF847

45 W, 870 MHz RF POWER TRANSISTOR NPN SILICON



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	16.5	Vdc
Collector-Base Voltage	V _{CBO}	38	Vdc
Emitter–Base Voltage	V _{EBO}	4.0	Vdc
Collector Current — Continuous	IC	12	Adc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	150 0.85	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C
Junction Temperature	TJ	200	°C

THERMAL CHARACTERISTICS

Characteristic		Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.17	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted.)

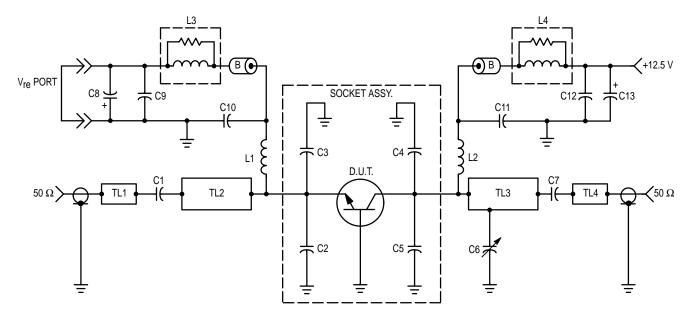
Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Emitter–Base Breakdown Voltage (I _E = 5.0 mAdc, I _C = 0)	V(BR)EBO	4.0	_	_	Vdc	
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, I _B = 0)	V(BR)CEO	16.5	_	_	Vdc	
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, V _{BE} = 0)	V(BR)CES	38	_	_	Vdc	
Collector Cutoff Current (V _{CE} = 15 Vdc, V _{BE} = 0)	ICES	_	_	10	mAdc	

(continued)



ELECTRICAL CHARACTERISTICS — **continued** $(T_C = 25^{\circ}C)$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS					
DC Current Gain (I _C = 2.0 Adc, V _{CE} = 5.0 Vdc)	hFE	40	65	120	_
DYNAMIC CHARACTERISTICS	•				
Output Capacitance (V _{CB} = 12.5 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	75	90	pF
FUNCTIONAL TESTS	•				
Common–Base Amplifier Power Gain (V _{CC} = 12.5 Vdc, P _{Out} = 45 W, f = 870 MHz)	GPB	4.5	5.5	_	dB
Collector Efficiency (V _{CC} = 12.5 Vdc, P _{Out} = 45 W, f = 870 MHz)	ης	60	68	_	%
Load Mismatch (V _{CC} = 15.5 Vdc, P _{in} = 16 W, f = 870 MHz, VSWR = 10:1, All Phase Angles)	Ψ	No Degradation in Output Power			



C1 — 51 pF, 100 mil Chip Capacitor

C2 — 12 pF, Mini-Underwood

C3 — 11 pF, Mini–Underwood

C4, C5 — 21 pF, Mini–Underwood

C6 — 0.08-8.0 pF Johansen Gigatrim

C7 — 47 pF, 100 mil Chip Capacitor

C8, C13 $\stackrel{\cdot}{-}$ 10 μ F, 25 WV Electrolytic Capacitor

C9, C12 — 1000 pF Unelco J101

C10, C11 — 91 pF Mini-Underwood

L1, L2 — 4 Turns #18 Enameled, 200 mil ID

L3, L4 — 12 Turns #22 Enameled, Wound Over 10 Ω Resistor

TL1, TL4 — 50 Ω Microstrip Line

TL2 — Microstrip ($Z_0 = 38$ ohms, $\lambda/4$ @ 838 MHz)

TL3 — Microstrip ($Z_0 = 28 \text{ ohms}, \lambda/4 @ 838 \text{ MHz}$)

Board Material — 0.032" Glass–Teflon, 2 oz. cu. clad, ϵ_{r} = 2.56

B — Ferrite Bead, Ferroxcube 56-590-65-3B

Figure 1. 806-870 MHz Broadband Test Circuit

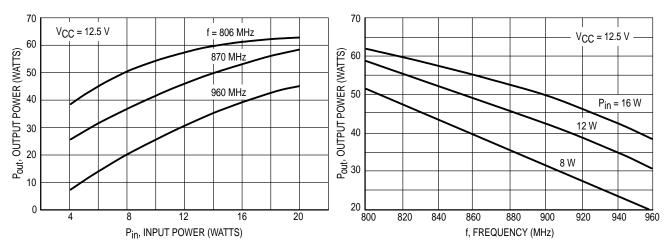


Figure 2. Output Power versus Input Power

Figure 3. Output Power versus Frequency

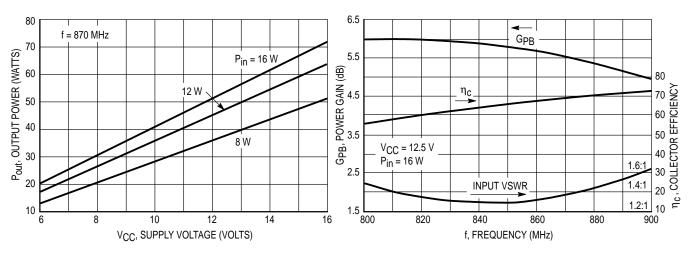
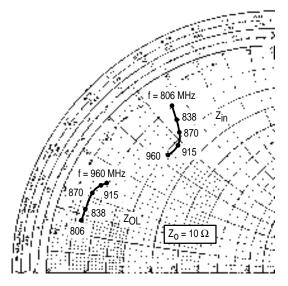


Figure 4. Output Power versus Supply Voltage

Figure 5. Typical Broadband Circuit Performance



 V_{CC} = 12.5 Vdc, P_{in} = 16 W, P_{out} = 45 W

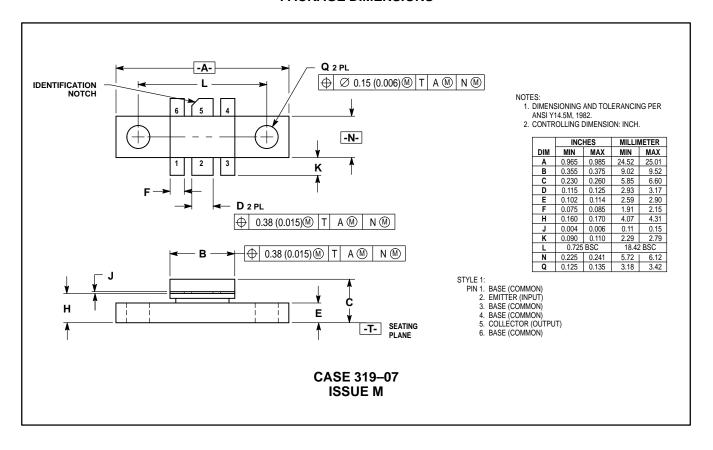
f MHz	Z _{in} (Ohms)		f MHz	Z _{OL} * (Ohms)	
806	0.99	+j5.52	806	0.67	+j1.33
838	1.48	+j5.47	838	0.68	+j1.66
870	1.79	+j5.25	870	0.72	+j2.16
915	2.12	+j4.80	915	0.83	+j2.40
960	2.11	+j4.28	960	0.99	+j2.50

 Z_{OL}^* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

Figure 6. Series Equivalent Input/Output Impedances

MOTOROLA RF DEVICE DATA **MRF847**

PACKAGE DIMENSIONS



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