The RF Line NPN Silicon RF Power Transistor

- ... designed for power amplifier applications in industrial, commercial and amateur radio equipment to 30 MHz.
- Specified 12.5 Volt, 30 MHz Characteristics —
 Output Power = 60 Watts
 Minimum Gain = 13 dB
 Efficiency = 55%

MATCHING PROCEDURE

In the push–pull circuit configuration it is preferred that the transistors are used as matched pairs to obtain optimum performance.

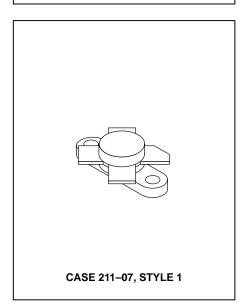
The matching procedure used by Motorola consists of measuring hpe at the data sheet conditions and color coding the device to predetermined hpe ranges within the normal hpe limits. A color dot is added to the marking on top of the cap. Any two devices with the same color dot can be paired together to form a matched set of units.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	VCEO 18	
Collector–Emitter Voltage	VCES	36	Vdc
Emitter–Base Voltage	VEBO	4.0	Vdc
Collector Current — Continuous	IC	15	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	175 1.0	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

MRF455

60 W, 30 MHz RF POWER TRANSISTOR NPN SILICON



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.0	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted.)

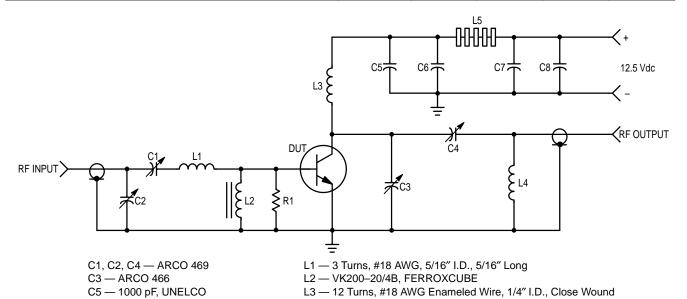
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				
Collector–Emitter Breakdown Voltage (IC = 100 mAdc, IB = 0)	V(BR)CEO	18	_	_	Vdc
Collector–Emitter Breakdown Voltage (IC = 50 mAdc, VBE = 0)	V(BR)CES	36	_	_	Vdc
Emitter–Base Breakdown Voltage (I _E = 10 mAdc, I _C = 0)	V(BR)EBO	4.0	_	_	Vdc
ON CHARACTERISTICS					
DC Current Gain (I _C = 5.0 Adc, V _{CE} = 5.0 Vdc)	hFE	10	_	150	_
DYNAMIC CHARACTERISTICS	-				
Output Capacitance (V _{CB} = 12.5 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	_	250	pF

(continued)



ELECTRICAL CHARACTERISTICS — **continued** ($T_C = 25$ °C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
FUNCTIONAL TESTS (Figure 1)					
Common–Emitter Amplifier Power Gain (V _{CC} = 12.5 Vdc, P _{Out} = 60 W, f = 30 MHz)	G _{pe}	13	_	_	dB
Collector Efficiency (V _{CC} = 12.5 Vdc, P _{out} = 60 W, f = 30 MHz)	η	55	_	_	%
Series Equivalent Input Impedance (V _{CC} = 12.5 Vdc, P _{Out} = 60 W, f = 30 MHz)	Z _{in}	_	1.66-j.844	_	Ohms
Series Equivalent Output Impedance (V _{CC} = 12.5 Vdc, P _{Out} = 60 W, f = 30 MHz)	Z _{out}	_	1.73–j.188	_	Ohms
Parallel Equivalent Input Impedance (V _{CC} = 12.5 Vdc, P _{Out} = 60 W, f = 30 MHz)	Z _{in}	_	2.09/1030	_	Ω/pF
Parallel Equivalent Output Impedance (V _{CC} = 12.5 Vdc, P _{Out} = 60 W, f = 30 MHz)	Z _{out}	_	1.75/330	_	Ω/pF



R1 — 10 Ohm/1.0 Watt, Carbon

Figure 1. 30 MHz Test Circuit Schematic

L4 — 3 Turns 1/8" O.D. Copper Tubing, 3/8" I.D., 3/4" Long

L5 — 7 FERRITE Beads, FERROXCUBE #56-590-65/3B

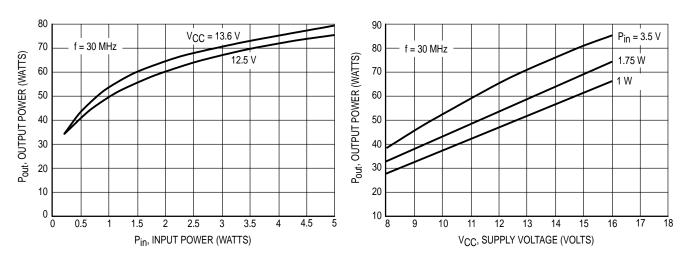


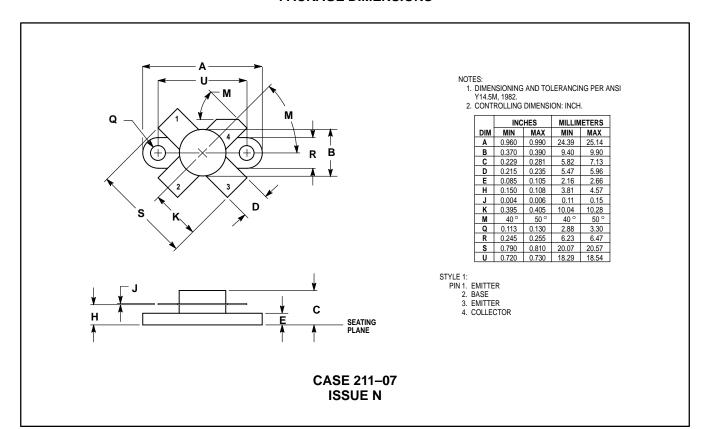
Figure 2. Output Power versus Input Power

C6, C7 — 0.1 µF Disc Ceramic

 $C8 - 1000 \, \mu F/15 \, V \, Electrolytic$

Figure 3. Output Power versus Supply Voltage

PACKAGE DIMENSIONS



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